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Coherence Function Method for Detecting Inter-Turn Fault in HVDC Converter Transformer

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ABSTRACT: This paper intends to detect the inter-turn fault by performing impulse test on HVDC Converter transformer by Coherence Function method. The inter-turn in a transformer (due to insulation failure) is located in impulse test by neutral current method. In the neutral current method, a record of the impulse current flowing through a resistive shunt between the neutral and ground point is used for detecting the fault. Neutral current so measured or calculated is in time domain. Theoretically the calculated neutral current, both for healthy winding and turn-fault winding is subjected to FFT to convert time domain into frequency domain of real and imaginary values. The real and imaginary values are used to calculate the coherence function. Coherence function compares both with and without fault neutral current in frequency domain, so that fault can be identified. Coherence function compares input and output at each frequency.It displays output i.e coherence value as 1 if input and output at that frequency is same other wise less than 1.1 indicates that there is no fault at that frequency and decreasing coherence value (<1) indicates fault at that frequency.

KEYWORDS: HVDC converter transformer, Coherence function, FFT.

I.INTRODUCTION

HVDC transmission has been gaining popularity ever since its first commercial operation in 1954. The advent of HVDC technology [1] has been so rapid that it is widely applied all over the world for bulk power transmission over long distances. It is popularly employed for interconnecting two asynchronous systems not only through overhead lines but also through submarine cables. The power supply is made available to islands and remote places by means of HVDC Transmission.

HVDC systems are also gaining importance because of the increased use of renewable energy resources for power generation. HVDC systems are used to transmit electricity from remotely located non conventional energy sources to thickly populated cities. Even in conventional power stations, it is easier to transmit electricity by HVDC from a remotely located power station closer to the location of the coal or natural gas, rather than to set up a power station closer to the thickly populated city and to transport coal from remotely located coal mines.

The converter transformer is one piece of vital equipment in the HVDC transmission system. It serves as the isolating device between the power transformer and the dc link and step-down the voltage as required by the thyristor converters. It is equipped with on load tap changers on the primary side to maintain the ac voltage supplied to the thyristor converters constant at all conditions. But frequent failure of the converter transformers a major cause for concern to the electric power utilities all over the world. Invariably, it is found that the secondary winding of the converter transformer is the one that fails. Generally, 12 pulse thyristor converters are employed in HVDC transmission to eliminate the fifth and seventh harmonics and this is achieved by connecting the secondary windings in Y- fashion to introduce a 30 phase shift. But the fifth and seventh harmonics are very much present in the secondary windings although they are absent in the primary side. The secondary windings should have been designed to withstand these harmonic contents.

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The failure analysis of HVDC systems reported by CIGRE [4] states that out of 22 failures in the last few years, 14 failures were secondary winding failures. In India, almost all failures have taken place on the secondary windings of the converter transformers. Such failures have been attributed to corrosive oil-forming copper sulphide sediments, voltage transients arising during the commutation process, and temperature rise.

It has also been recommended that repetitive voltage transients initiate partial discharge and eventually result in the failure of the converter transformer [5], [6]. The harmonic leakage fluxes because thermal problems in the converter transformer which may lead to its failure as discussed in [7]. Grant and Mc Dermid in [8] confirmed that a converter transformer shows signs of insulation degradation due to thermal aging after a few decades of normal operation. In this paper, an attempt has been made to analyse the HVDC system from an electrical power engineer's point of view to realize the origin reason for the failure of the converter transformer secondary. Some solutions have also been suggested to eliminate these problems.

II. HVDC CONVERTER TRANSFORMER AND IMPULSE TESTING

The function of the impulse test is to find out the capability of insulation of the transformers to bare the transient voltages due to lightning, As transients are impulses of short rise time, the voltage distribution along the transformer winding does not stay uniform for quick rising voltages. Fault investigation and diagnostics of HVDC converter transformers are essential, as power quality and reliability is one of the important factors for the electric power system. Insulation failure within transformer is treated to be one of the most important causes of complete failure of HVDC transformers. In order to evaluate the reliability of the winding, specific high voltage test is conducted on the transformer. To avoid large over-voltages being induced in the winding, components which are not under test short circuit and connected to ground.

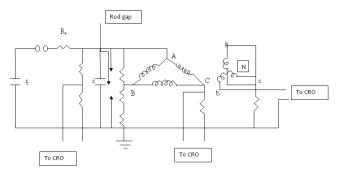


Fig.2.1.Scematic diagram of transformer under test

The schematic diagram of the transformer connection for high voltage impulse testing is shown in figure 2.1. In transformer testing, it is necessary to record the wave forms of the applied voltages and currents from the winding which is under test. It is also important that the grounding must be proper and the windings which are not under test are correspondingly terminated.

III. TECHNICAL DATA AND RATINGS OF ALL EQUIPMENTS USED IN HIGH VOLTAGE TESTIMPULSE GENERATOR

Impulse generators used for testing must have adequate capacitance per stage so that it does not cause loading of impulse generator during test. Typically for a 400kV transformer where lighting impulse test voltage 1425kV, the impulse generator rating could be of the order of 2MV, stage capacitance 0.5uF, charging voltage 200kV/stage, having 10 stages a total energy of 100kJ. The present study is on the basis of theoretical calculation and voltage applied for study is unity. The data can be extrapolated for the star winding impulse test voltage of 1550kV.

A. MEASURING SYSTEM: Measuring system comprises an impulse type digital oscilloscope. The current signal from the neutral is made to pass through a current shunt and voltage is also recorded. However in the present study only theoretically calculated data has been used.

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B. TEST TRANSFORMER: Single phase of the HVDC converter transformer of power ratings 315MVA, $400/\sqrt{3}$ /206/ $\sqrt{3}$ kV is shown in the___ Fig.3.1. Figure shows the constructional geometry of one limb, comprising Tapping, HVAC and HVDC winding. For the purpose of neutral current calculations, the tapping winding has been fully earthed as is done in impulse test.

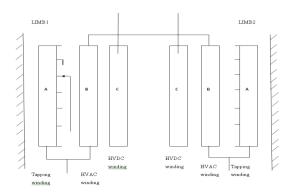


Fig.3.1.HVDC converter transformer under test

C. FORMULATION OF EQUIVALENT ELECTRICAL NETWORK: HVDC winding has been considered for analysis which has 246 discs. The HVAC windings are divided into 8 sections and HVDC windings are divided into 16 sections. Number of turns in each section is given by 246/16=15.4. Based on the no of turns in the section, paper insulation thickness, physical clearance between the windings and ducts, the calculations of self and mutual inductances, series and ground capacitance have been carried out. The network is suitably formed. The network is solved for Eigen Voltages values and frequency and finally the neutral currents are calculated. (The above data is obtained from BHEL R&D). Fig 3.2. Shows the schematic diagram of the three windings of one limb of HVDC transformer.

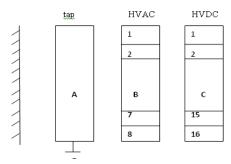


Fig.3.2. Sectional distribution of HVDC converter transformer

IV. DETECTION OF FAULT DURING IMPULSE TESTING

The inter-turn in a transformer (due to insulation failure) is located in impulse test by neutral current method. In the neutral current method, a record of the impulse current flowing through a resistive shunt between the neutral and ground point is used for detecting the fault. Neutral current so measured or calculated is in time domain.

The natural frequencies of an electrical circuit comprising parameters like resistance, inductances and capacitances can alter only in the event of a change in the parameters. The frequencies are independent of the magnitude and shape of applied voltage. Thus, the frequency spectrum is not altered by the types of surge voltages. When inter-turns fault occurs in a transformer winding, then the neutral current value undergoes change because of change in R, L, C of the network. Here we can use coherence function as a tool for detection of inter-turn fault by theoretically calculating neutral current obtained after subjecting to a unit impulse to one of the limb of star winding of a HVDC converter transformer. Theoretically the calculated neutral current, both for healthy winding and turn-fault winding is subjected

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to FFT [Fast Fourier Transformation] for determination of dominant frequencies. FFT is used to convert time domain into frequency domain of real and imaginary values. The real and imaginary values are used to calculate the coherence function. Coherence function compares both with and without fault neutral current in frequency domain, so that fault can be identified.

Let's assume that f(t) and g(t) are the input and output signals of the system and, F(f) and G(f) the related complex Fourier Transforms. The coherence function is mathematically defined as:

$$\begin{split} &\gamma^{2}(f) = \frac{\left|S_{FG}(f) \cdot S_{GG}(f)\right|}{\left|S_{FF}(f) \cdot S_{GG}(f)\right|} = \\ &= \frac{\left|F^{*}(f) \cdot Gf\right|^{2}}{\left[F^{*}(f) \cdot F(f)\right] \left[G^{*}(f) \cdot G(f)\right]} = \\ &= \frac{\left[\operatorname{Re}(F(f)) - J\operatorname{Im}(F(f))\right] \cdot \left[\operatorname{Re}(G(f)) + J\operatorname{Im}(G(f))\right]^{2}}{\left[\operatorname{Re}^{2}(F(f)) + \operatorname{Im}^{2}(F(f))\right] \cdot \left[\operatorname{Re}^{2}(G(f)) + \operatorname{Im}^{2}(G(f))\right]} = \\ &= \frac{\left[\operatorname{Re}(F(f)) \cdot \operatorname{Re}(G(f)) + \operatorname{Im}(F(f)) \cdot \operatorname{Im}(G(f))\right] + J\left[\operatorname{Re}(F(f)) \cdot \operatorname{Im}(G(f)) - \operatorname{Im}(F(f)) \cdot \operatorname{Re}(G(f))\right]^{2}}{\left[\operatorname{Re}^{2}(F(f)) + \operatorname{Im}^{2}(F(f))\right] \cdot \left[\operatorname{Re}^{2}(G(f)) + \operatorname{Im}^{2}(G(f))\right]} = \end{split}$$

The Fast Fourier transform (FFT) spectrum analyser gives the computation and display of the coherence function conjunction with the output spectrum to allow an efficient comparison between the coherence function variants and the related output spectra components.

V. IMPLEMENTATION OF COHERENCE FUNCTION IN INTER-TURN FAULT DETECTION

A suitable computer programme has been developed to coherence function for fault determinations. To identify fault calculations we substitute the real i.e. Re(F(f)) and imaginary i.e. Im(F(f)) values of neutral current through a healthy winding at a particular frequency (f) after determining FFT as input to the coherence function equation i.e. $Re(\gamma 2)$ and real i.e. Re(G(f)) and imaginary i.e. Im(G(f)) values of neutral current through a faulty winding at a particular frequency(f) after determining FFT as output to coherence equation.

Coherence function as a tool compares input and output at each frequency. It displays output i.e coherence value as 1 if input and output at that frequency is same. Otherwise less than 1. 1 indicates that there is no fault at that frequency and decreasing coherence value (<1) indicates fault at that frequency.

If the coherence value is 1 at all frequencies then it indicates that there is no fault in the winding. Thus it implies that the winding has passed the standard test. A typical plot of Coherence function for fault in the winding and with no fault for a typical current is discussed in detail.

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VI. RESULTS

A typical neutral current signal from a healthy winding and its corresponding Fourier Transform is shown in Fig.7.1.

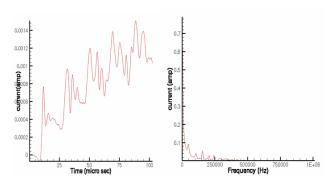


Fig 7.1: Neutral current in Time and frequency domains without fault

Neutral current with single turn fault and it Fourier transform is shown in Fig.7.2.

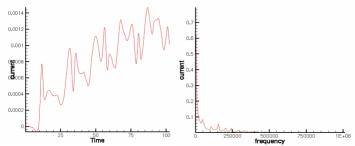


Fig 7.2: Neutral current in Time and frequency domains with single-Turn fault

Fig.7.3. shows the Coherence Function output signal when both given input and output functions are same signal. Since the signal is same, the imaginary value of the Coherence Function is zero and all real values are unity. The Coherence Function with single turn fault is shown in Fig.7.3.

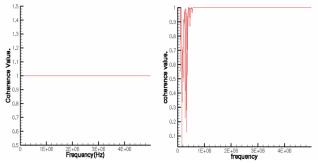


Fig 7.3: Coherence Function without and with single turn fault

It can be seen that corresponding to the frequencies there is dip in Coherence Function. Similarly a neutral current with two turn fault and middle turn fault and its corresponding Fourier transforms is shown in Fig.7.4

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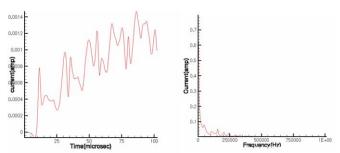


Fig 7.4: Neutral current in Time and frequency domains with Two-Turn fault

A neutral current with two turn fault and middle turn fault and its corresponding Fourier transforms is shown in Fig.7.5

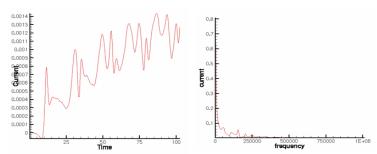


Fig 7.5: Neutral current in Time and frequency domain signals when fault is at middle of the winding

Whereas Fig. 7.6. Shows the coherence functions of the two turn and middle turn faults respectively.

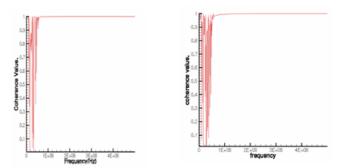


Fig 7.6: Coherence Function for two-turns & fault at the middle of winding

VII.CONCLUSION

315MVA ,400/ $\sqrt{3}/206/\sqrt{3}$ kV transformer has been considered for analysis of neutral current, Fourier Transform and Coherence Function with a view to detect fault, theoretical simulation has been carried out by creating single turn, 2 turn fault at the top of the winding and also in the middle region of the winding. For every simulation fault current is calculated, the corresponding FFT is computed. Based on FFT values of current without fault and with fault Coherence Function is calculated. An examination of Coherence Function suggests that there is a change in value which differs from unity. The distant of change from unity represents the severity of fault. However, the severity cannot be quantified by value of Coherence Function, since the fault causes reduction in inductance of the coil and reduction in series and parallel capacitance. While reduction in inductance causes increase in neutral current, reduction in parallel capacitance causes reduction in current Hence, direct relationship between magnitudes of current and hence magnitude of coherence value cannot be established with confidence. It is expected that reduction in Δ f may suggest the depth of

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severity of fault. More computation needs to be carried out by simulating fault along coil depth (CD) of the HVDC winding. In order to reduce the error in computation it is desirable to calculate current for longer duration. However, the Coherence check can prove to be a good tool for determining fault in the winding.

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BIOGRAPHIES



Koganti sri lakshmi was born in India in 1983. She received the B.Tech. and M.Tech. Degrees in electrical engineering from the JNTU University, Hyderabad, A.P., India, in 2006 and 2009, respectively, currently, she is working as Assistant Professor in Electrical Engineering department of SNIST. Her current research interests include Renewable energy, HVDC, Smart Grid, power quality, FACTS, and HVAC.



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