



A High Step-Up DC/DC Converter Based on Integrating Coupled Inductor Technique

Anju C Thannickal¹, Jojin Thomas²

PG Student [PE], Dept. of EEE, St. Joseph's College of Engineering and Technology, Palai, Kerala, India¹

Assistant Professor, Dept. of EEE, St. Joseph's College of Engineering and Technology, Palai, Kerala, India²

ABSTRACT: This paper proposes a high step-up dc-dc converter based on integrating coupled inductor. The converter consists of a coupled inductor and two voltage multiplier cells, in order to obtain a high voltage gain. The converter achieves large voltage conversion ratio with appropriate duty cycle and also reduces voltage stress on power devices. The capacitors are charged using energy stored in coupled inductor and increases the voltage transfer gain. . This paper investigates a coupled inductor integrated high step-up dc-dc converter. The proposed converter can step up low dc voltage into a high dc voltage. The topology of proposed converter can achieve large conversion ratio and high efficiency.

KEYWORDS: DC/DC Converter, high step-up, coupled inductor.

I. INTRODUCTION

Renewable energy sources have experienced a fast development in recent years. Distributed generation systems based on renewable energy systems include photovoltaic cells, fuel cells, wind power etc. In order to overcome such adverse effects, this distributed energy systems can be utilized by the high step up converter to produce high voltage and satisfy the demands. Conventional boost converters can't provide such a high DC voltage gain for extreme duty cycle. Thus high step up dc-dc converters are used as front end converters to step from low voltage to high voltage which are required to have a large conversion ratio, high efficiency and small volume. In some converters active clamp circuit is used to overcome voltage spikes caused by the leakage inductance of the coupled inductor. Low level voltage from battery is connected to Kilo watt level using step up dc/dc converter and inverter circuits. Voltage spikes and switching losses are eliminated by active clamping. In dc-ac, inverter always tends to draw ac ripple current at twice the output frequency. In some converters high voltage conversion is obtained by changing transformer turns ratio which will increase the overall efficiency but still the operation of main switch involves hard switching, reverse recovery problem and also EMI noise gets raised. Here, the voltage boost is done without a transformer and a high voltage gain is achieved without an extremely high duty ratio. The energy stored in leakage inductance is recycled with the use of passive clamp circuit. The voltage stress of main power switch is also reduced in this topology.

II. SYSTEM MODEL AND WORKING

The proposed high step-up dc/dc converter based on integrating coupled inductor technique is shown in figure 1. Here the converter operates using a single switch without zero switching network and complex control. The proposed converter comprises a dc input voltage (V_1), active power switch (S), coupled inductor, four diodes, and four capacitors. The clamp circuit is constituted by capacitor C_1 and diode D_1 . The capacitor C_3 is employed as the capacitor of the extended voltage multiplier cell. The capacitor C_2 and diode D_2 are employed to increase the voltage of clamping capacitor C_1 .

International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 9, September 2016

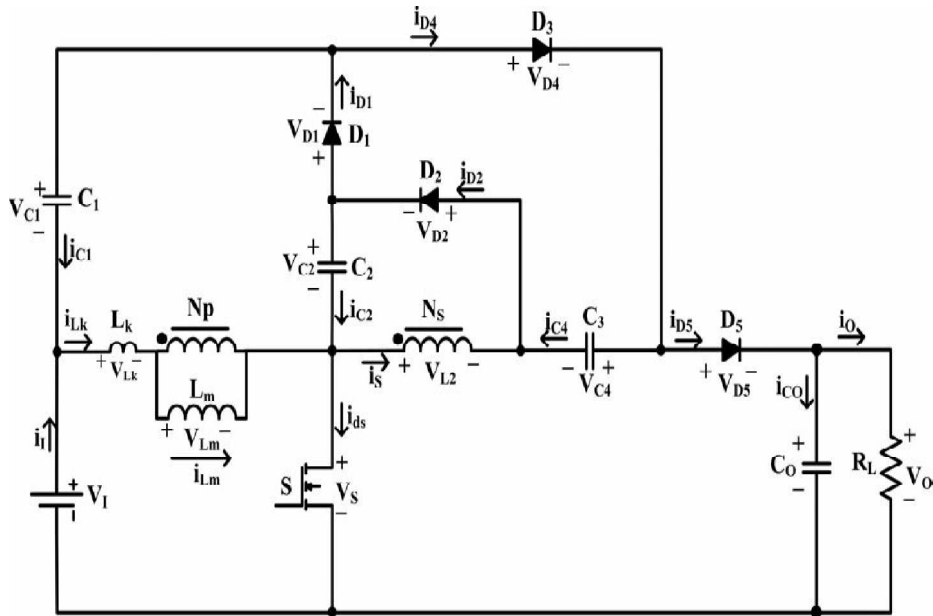


Fig. 1 High step-up converter based on integrating coupled inductor

The high step-up dc/dc converter based on integrating coupled inductor has five modes of operation.
MODE 1

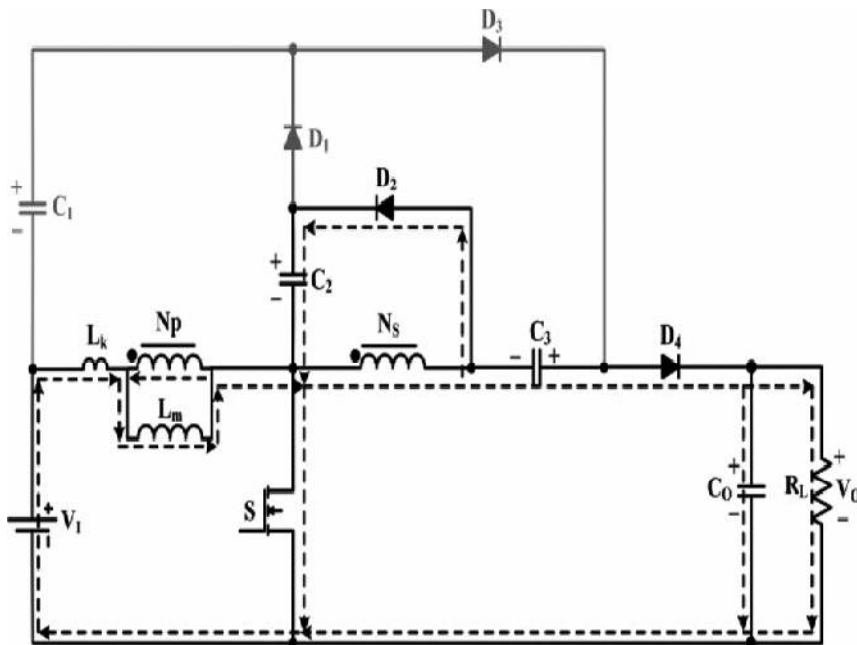


Fig. 2. Mode 1 operation of high step-up converter based on integrating coupled inductor

In this mode, Switch S, diodes D₂ and D₄ are turned on. The leakage inductor current increases linearly and secondary side current of coupled inductor decreases linearly. The output capacitor C₀ supplies required energy of the load. The dc source magnetises L_m through main switch S.

MODE 2

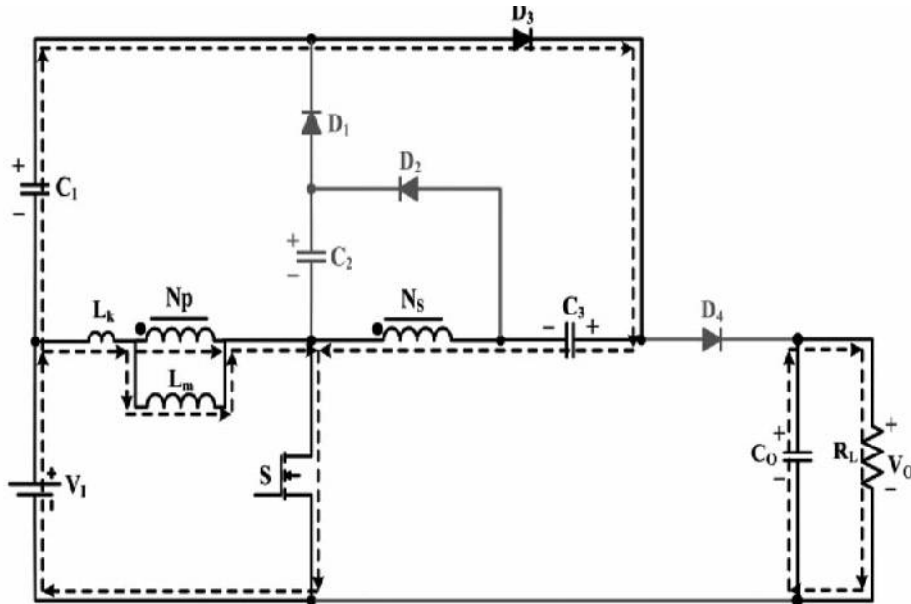


Fig. 3. Mode 2 operation of high step-up converter based on integrating coupled inductor

In this mode, switch S and diode D₃ are turned ON and diodes D₁, D₂, and D₄ are turned OFF. The current of the magnetizing inductor L_M and leakage inductor L_K increase linearly. The capacitor C₃ is charged by dc source, clamp capacitor and the secondary-side of the coupled inductor. Output capacitor supplies the demanded energy of the load.

MODE 3

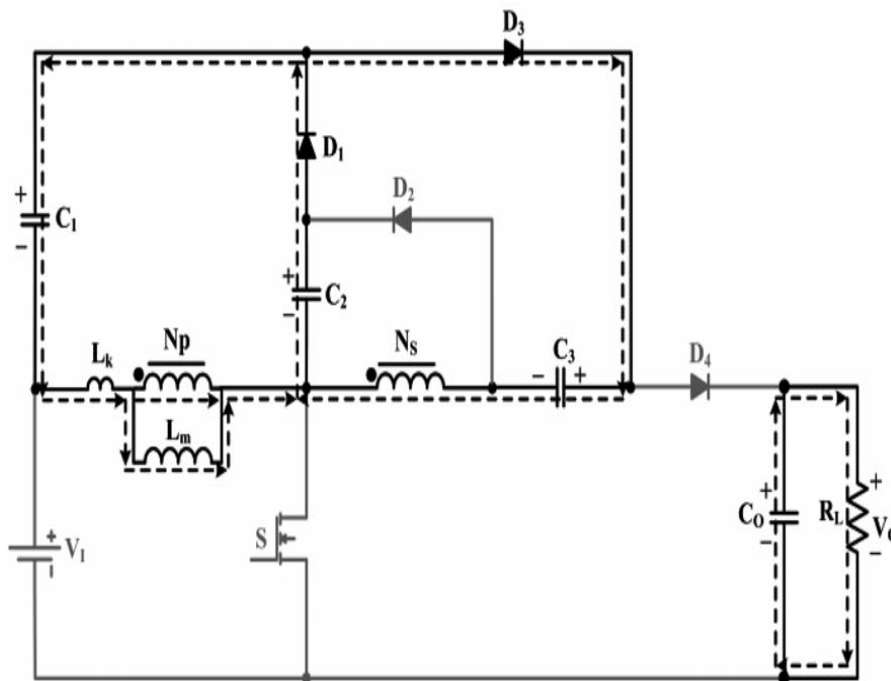


Fig. 4. Mode 3 operation of high step-up converter based on integrating coupled inductor

In this mode, Diodes D₁ and D₃ are turned ON and diodes D₂ and D₄ are turned OFF. The stored energy in capacitor C₂ and the energies of leakage inductor and magnetizing inductor charges clamp capacitor C₁.

MODE 4

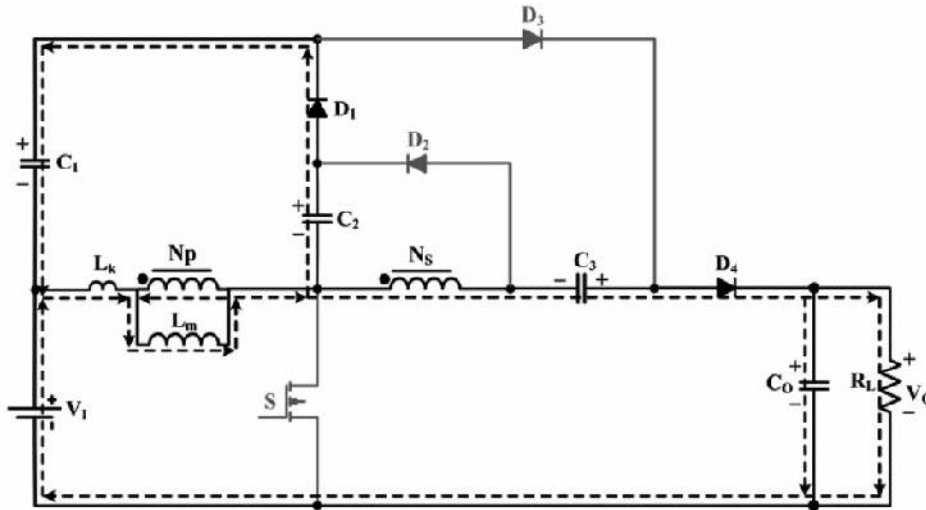


Fig. 5. Mode 4 operation of high step-up converter based on integrating coupled inductor

In this mode, the leakage inductor and magnetising inductor current decreases linearly. The dc source V_1 , capacitor C_3 and either sides of the coupled inductor charge output capacitor C_0 and provide energy to the load .

MODE 5

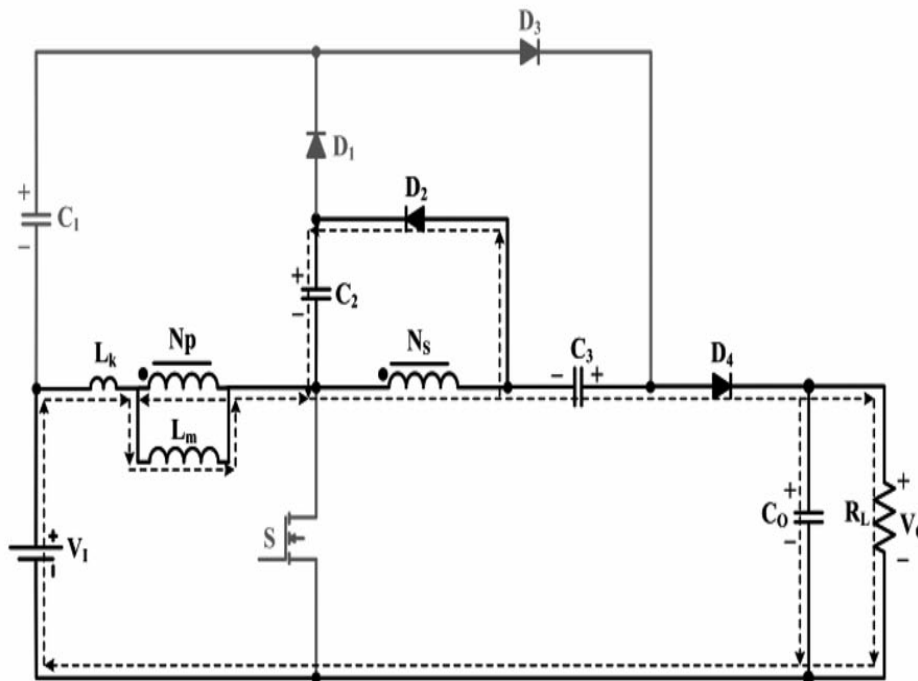


Fig. 6. Mode 5 Operation of high step-up converter based on integrating coupled inductor

In this mode, Diodes D_2 and D_4 are turned ON and diodes D_1 and D_3 are turned OFF. The energy stored in L_m is transferred to secondary side of coupled inductor to charge C_2 . The dc input voltage and stored energy in the capacitor C_3 and inductances of both sides of the coupled inductor charge the output capacitor and provide the demand energy to the load .

International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 9, September 2016

III.SIMULATION

The simulation result was done in MATLAB/Simulink. The Simulink model of the high step-up dc/dc converter based on integrating coupled inductor technique is shown in the Fig. 7. The output voltage waveform is obtained. Closed loop control is adopted for the proposed system. Closed loop control schemes are more accurate than open loop system due to their complex construction. They are equally accurate and are not disturbed in the presence of non-linearities. Because of a feedback mechanism, so they clear out the errors between input and output signals, and hence remain unaffected to the external noise sources. The closed loop PI controller is used in the high step-up dc/dc converter based on integrating coupled inductor technique to achieve the desired output voltage.

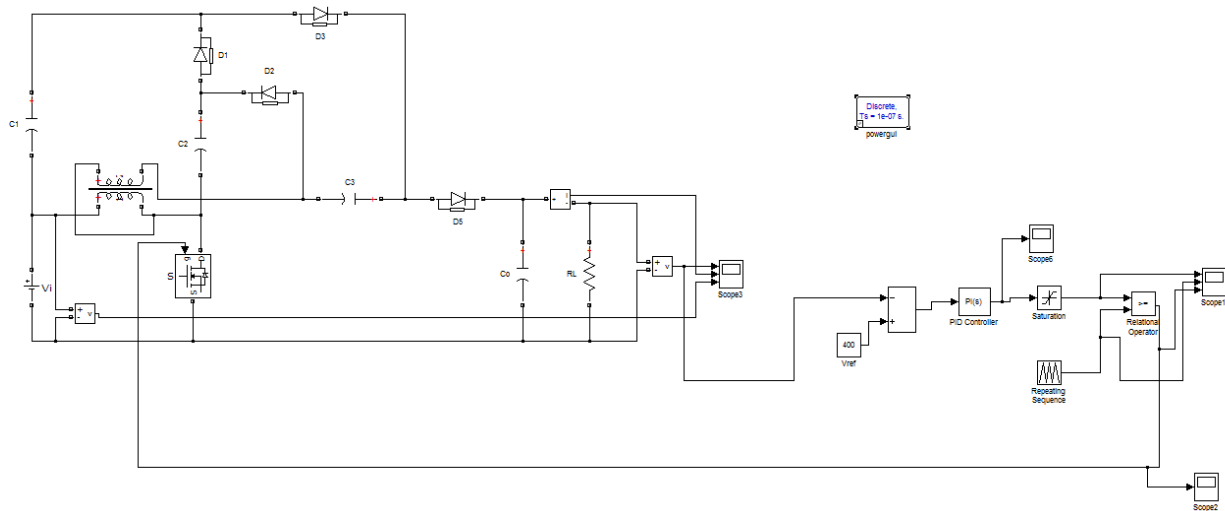


Fig. 7 Simulink model of high step-up dc/dc converter based on integrating coupled inductor

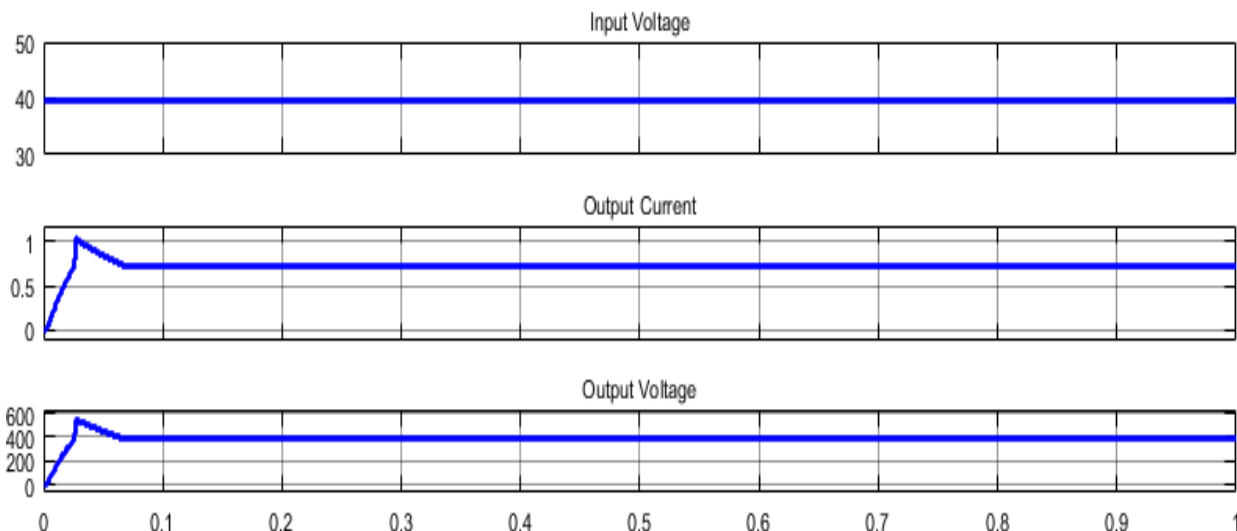


Fig. 9 Simulation results of high step-up dc/dc converter based on integrating coupled inductor

Here the output for proposed converter is designed for 400V. The input DC voltage is taken as 40 V. The voltage across switch are shown in figure 10 .

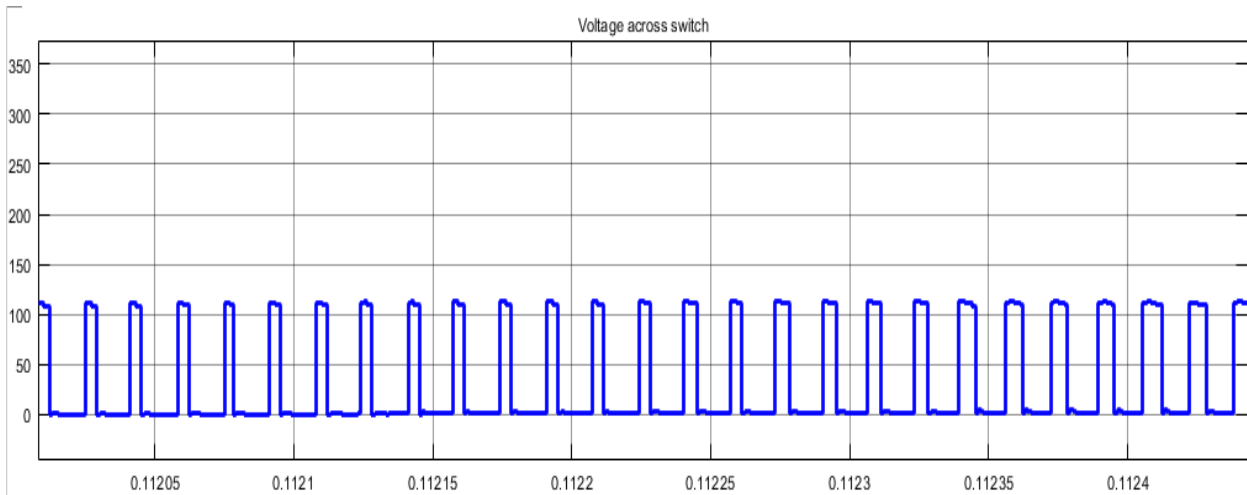


Fig. 10 Voltage across main switch S

IV.HARDWARE IMPLEMENTATION

Hardware implementation of the High step-up dc/dc converter based on integrating coupled inductor is given in the figure 12.

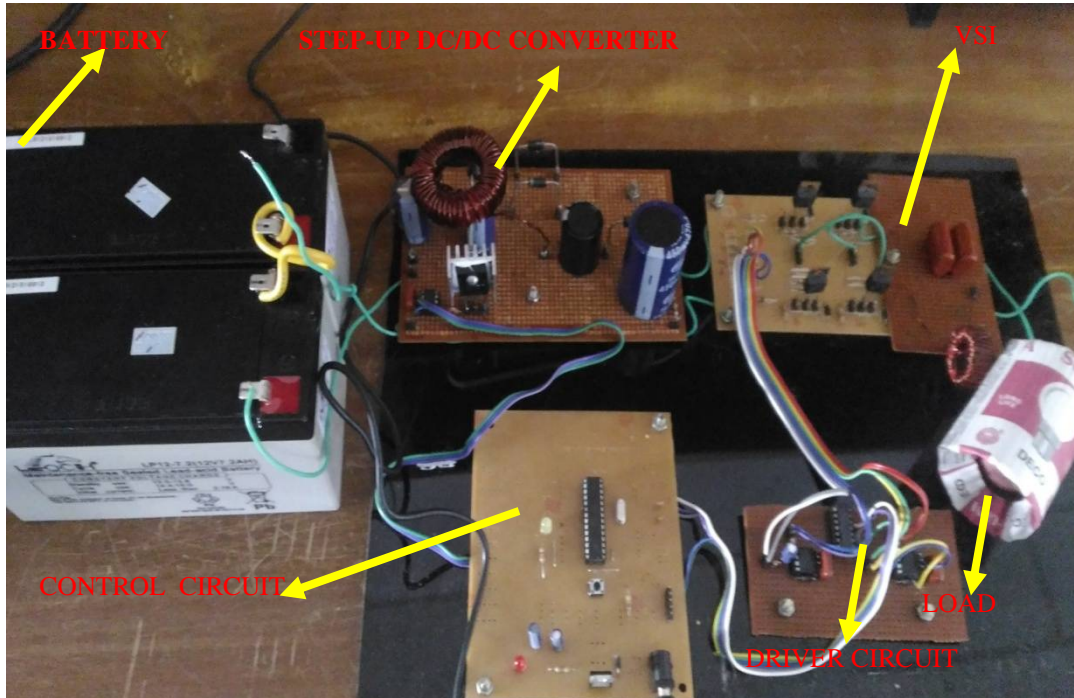


Fig. 11 Hardware implementation of the high step-up dc/dc converter based on integrating coupled inductor

The hardware setup consists of high step-up dc/dc converter, driver circuit, and control circuit. To perform the various operations and conversions required to switch, control and monitor the devices a processor is needed. The processor chosen is ATmega16. The gate pulses to the main switch S and to the inverter switches are given below.

International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 9, September 2016

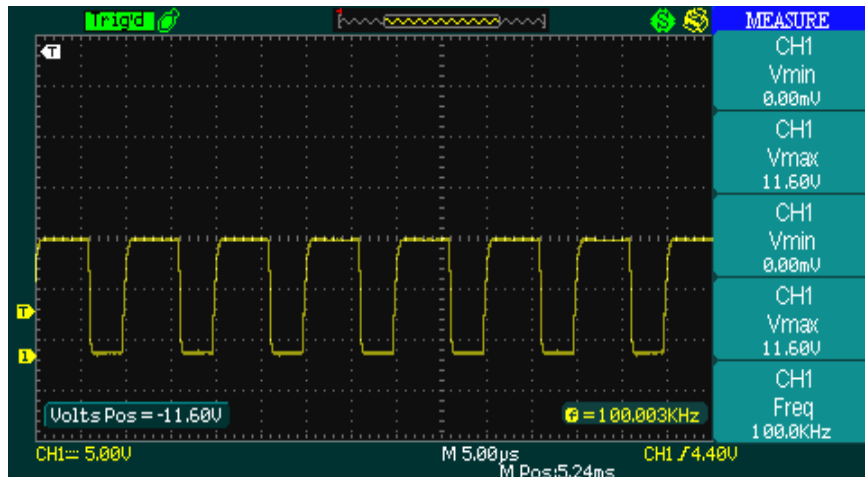


Fig. 12 Gate pulse for main switch S

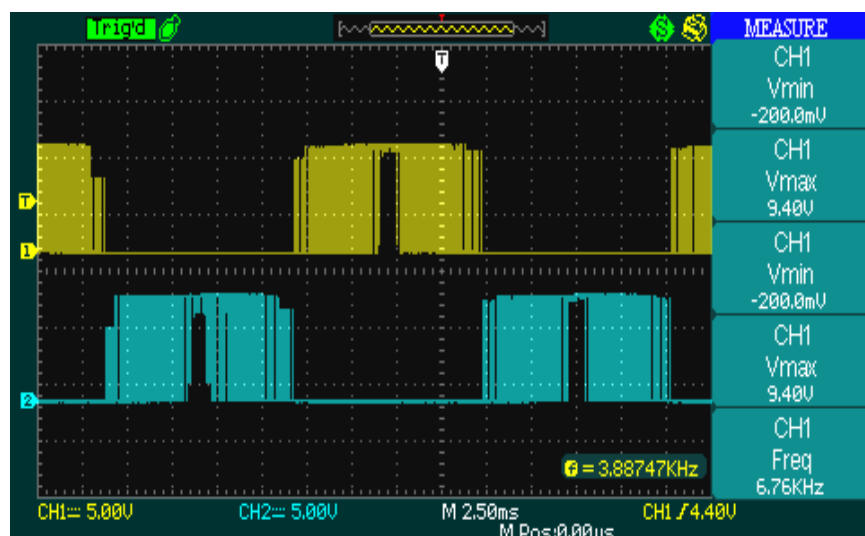


Fig. 13 Gate pulses for inverter switches

V. RESULT AND DISCUSSION

The proposed step down converter is shown in the figure 1 and its simulink model and hardware setup are given in figure 7 and 11 respectively. The converter is simulated for closed loop control strategy in MATLAB and their corresponding output voltages can be analysed. The high step-up dc/dc converter is simulated for input voltage of 40V to obtain a higher constant dc voltage of 400V .We get the simulation results which are given in the figures 9 and 10. Then we implement the hardware. The inverter SPWM frequency is at 7.8kHz and microcontroller works at 16MHz. Employing a higher frequency PWM input makes the response smoother by virtue of the fact that there is less time in the ON and OFF states for the circuit output to decay and rise, respectively .The gate pulses of converter and inverter for hardware setup of high step-up dc/dc converter based on integrating coupled inductor are given in figures 12 and 13.

VI.CONCLUSION

A high step-up dc/dc converter based on integrating coupled inductor technique is proposed in this paper. The coupled inductor integrated high step-up dc-dc converter proposed is suitable for distributed generation systems based on



International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 9, September 2016

renewable energy sources which require high voltage transfer gain. The voltage stress on the main power switch is reduced . The energy stored in the leakage inductance is recycled to improve the performance of the converter.

REFERENCES

- [1] Y. P. Hsieh, J. F. Chen, T. J. Liang, and L. S. Yang, "Novel high step-up DC-DC converter for distributed generation system," *IEEE Trans. Ind. Electron.*, vol. 60, no. 4, pp. 1473–1482, Apr. 2013.
- [2] K. I. Hwu and W. Z. Jiang, "Voltage gain enhancement for a step-up converter constructed by KY and buck-boost converters," *IEEE Trans. Ind. Electron.*, vol. 61, no. 4, pp. 1758–1768, Apr. 2014.
- [3] K. C. Tseng and C. C. Huang, "High step-up, high efficiency interleaved converter with voltage multiplier module for renewable energy system," *IEEE Trans. Ind. Electron.*, vol. 61, no. 3, pp. 1311–1319, Mar. 2014.
- [4] L. S. Yang, T. J. Liang, H. C. Lee, and J. F. Chen, "Novel high step-up DC-DC converter with coupled-inductor and voltage-doubler circuits," *IEEE Trans. Ind. Electron.*, vol. 58, no. 9, pp. 4196–4206, Sep. 2011.
- [5] T. F. Wu, Y. S. Lai, J. C. Hung, , "Boost converter with coupled inductors and buck-boost type of active clamp," *IEEE Trans. Ind. Electron.*, vol. 55, no. 1, pp. 154–162, Jan. 2008.
- [6] I. Laird and D. D. Lu, "High step-up DC/DC topology and MPPT algorithm for use with a thermoelectric generator," *IEEE Trans. Power Electron.*, vol. 28, no. 7, pp. 3147–3157, Jul. 2013.
- [7] W. Li, W. Li, X. Xiang, Y. Hu, and X. He, "High step-up interleaved converter with built-in transformer voltage multiplier cells for sustainable energy applications," *IEEE Trans. Power Electron.*, vol. 29, no. 6, pp. 2829–2836, Jun. 2014.
- [8] C. W. Chen, K. H. Chen, and Y. M. Chen, "Modeling and controller design of an autonomous PV module for DMPPT PV systems," *IEEE Trans. Power Electron.*, vol. 29, no. 9, pp. 4723–4732, Sep. 2014.
- [9] C. Olalla, C. Delineand, and D. Maksimovic, "Performance of mismatched PV systems with submodule integrated converters," *IEEE J. Photovoltaic*, vol. 4, no. 1, pp. 396–404, Jan. 2014.
- [10] J.H. Lee, T. J. Liang, and J. F. Chen, "Isolated coupled-inductor-integrated DC-DC converter with non-dissipative snubber for solar energy applications," *IEEE Trans. Ind. Electron.*, vol. 61, no. 7, pp. 3337–3348, Jul. 2014.
- [11] C. L. Wei and M. H. Shih, "Design of a switched-capacitor DC-DC converter with a wide input voltage range," *IEEE Trans. Circuits Syst.*, vol. 60, no. 6, pp. 1648–1656, Jun. 2013.
- [12] S. K. Changchien, T. J. Liang, J. F. Chen, and L. S. Yang, "Novel high step-up DC-DC converter for fuel cell energy conversion system," *IEEE Trans. Ind. Electron.*, vol. 57, no. 6, pp. 2007–2017, Jun. 2010.