

A Simplified Multi level Inverter Topology for Grid Interconnection of PV Systems

H R Ramesh¹, Dr. E.G. Shivakumar²Research Scholar, Dept. of EE, UVCE, Bengaluru, Karnataka, India¹Associate Professor, Dept. of EE, UVCE, Bengaluru, Karnataka, India²

ABSTRACT: Non-conventional energy sources (NCES) very important in now days because they are pollution free, easily accessible and enormous. Among NCES, Photovoltaic systems are mostly used as they are light, clean and easily installable. Normally PV cells converts sunlight into electricity in the form of dc. A suitable converter is usually needed to convert the dc power into ac power, which is then connected the power grid. The Multilevel Inverters [MLI] can be used to convert the Dc power Ac power for integration of renewable energy sources into the conventional grids. But the conventional MLIs such as Neutral point Clamped (NPC) MLIs requires extra diodes in with the active switches, Flying capacitor MLIs requires extra Capacitors whereas the Cascaded H-bridge MLIs requires separate dc sources . This paper proposes a simplified multi level Inverter which required less number of switches and lower THD as compared to conventional multilevel Inverters. The proposed Inverter can be used to integrate the Photovoltaic system into Grid, with satisfying the grid requirements such as phase angle, frequency and amplitude of the Grid voltage .Here Thirty three level proposed MLI is simulated using Matlab/Simulink by using equal area criteria which is very simple technique for generation of pulses to the switches and the results are presented.

KEYWORDS:Non-conventional energy sources (NCES), Multilevel inverter (MLI),Total harmonic distortion (THD).

I.INTRODUCTION

Non-conventional energy sources are alternatives to our conventional energy sources such as oil, coal, gas which are not renewable. The conventional energy sources are limited and they can be exhausted. Renewable energy sources are solar, wind, biomass, hydro, geothermal and ocean power. Among these solar energy is the more advantage because no pollution to the environment So, Photo voltaic (PV) systems are more useful.The basic element of a PV system is the solar cell .A solar cell is directly converts the solar energy into electrical energy in the form of direct current(DC). A typical PV cell consists of a p-n junction Diode as shown in the figure 1.

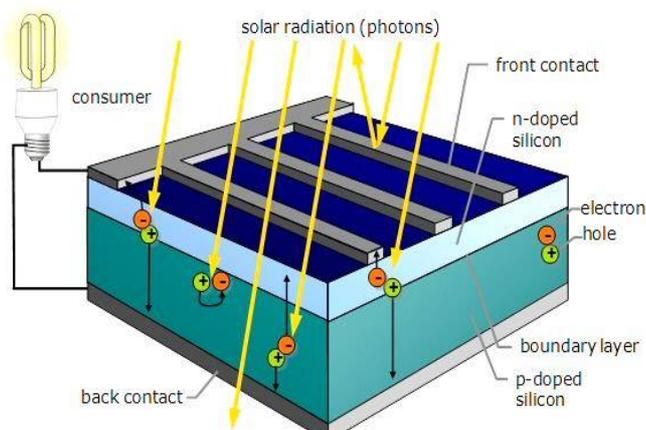


Fig.1. Basic Structure of a Solar cell

Grid interconnection of PV system requires an effective converter system to converter the DC power into AC power. The conventional H-bridge inverter produces a square wave output voltage, which contains higher harmonics but

International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 6, June 2016

Normal PWM inverter can reduce the THD, but increases the switching losses. As the importance of multilevel inverters [MLI] has been increased. The simplified types of multilevel inverters are suitable for high voltage and high power application because of their ability to generate waveform nearer to sine wave with less THD. Multilevel inverters are classified into three types: they are 1. Diode Clamped or neutral point (NPC) 2. Flying capacitor. 3. Cascaded H-bridge. NPC requires a large number of clamping diodes as the level increases. In flying capacitor, as the level increases, the cost also increases. Cascaded H-bridges are mostly preferably used [6] for high power applications. But it requires isolated DC sources, which increases the complexity of the topology. In order to overcome these disadvantages, this paper proposes a simplified type of multilevel inverter which requires a less number of DC sources and switches as compared to Cascaded H-bridge multilevel inverter. The THD of the output voltage can be achieved, which is in line with the IEEE standard. By using this topology, we can efficiently integrate the PV panel into the conventional power grid.

II. PROPOSED TOPOLOGY

The structure of the proposed simplified multilevel inverter is shown in Figure 2.

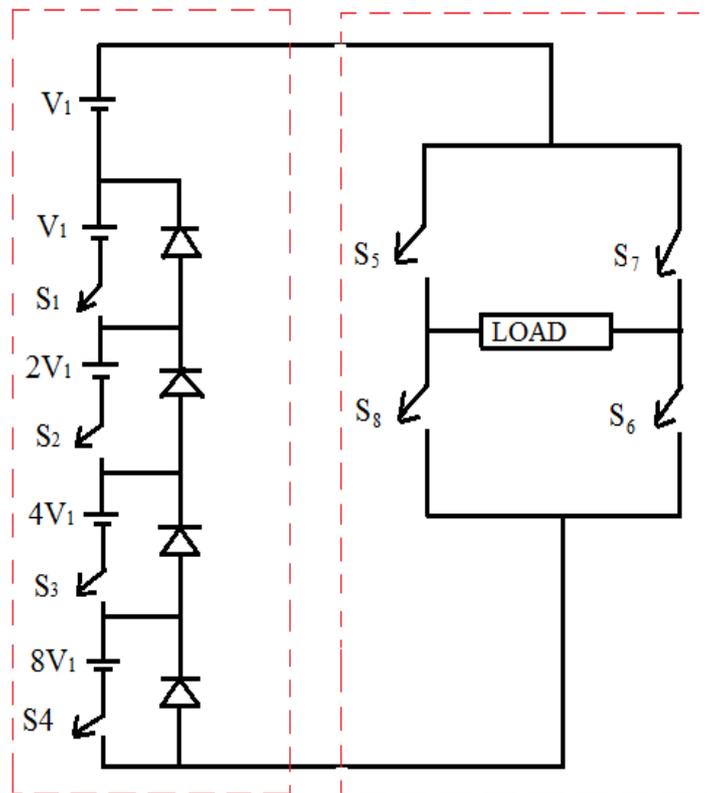


Fig.2. General Structure of proposed new multilevel inverter

III. THIRTY THREE LEVEL PROPOSED MULTILEVEL INVERTER

The thirty three level proposed inverter uses only eight switches and five separate DC sources with asymmetrical ratios having the ratios as, $V_1:V_1:2V_1:4V_1:8V_1$. Here V_1 is the minimum voltage.



International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 6, June 2016

SL.NO	S1	S2	S3	S4	S5	S6	S7	S8	OUTPUT VOLTAGE
1	1	1	1	1	1	1	0	0	16V1
2	0	1	1	1	1	1	0	0	15V1
3	1	0	1	1	1	1	0	0	14V1
4	0	0	1	1	1	1	0	0	13V1
5	1	1	0	1	1	1	0	0	12V1
6	0	1	0	1	1	1	0	0	11V1
7	1	0	0	1	1	1	0	0	10V1
8	0	0	0	1	1	1	0	0	9V1
9	1	1	1	0	1	1	0	0	8V1
10	0	1	1	0	1	1	0	0	7V1
11	1	0	1	0	1	1	0	0	6V1
12	0	0	1	0	1	1	0	0	5V1
13	1	1	0	0	1	1	0	0	4V1
14	0	1	0	0	1	1	0	0	3V1
15	1	0	0	0	1	1	0	0	2V1
16	0	0	0	0	1	1	0	0	V1
17	0	0	0	0	0	0	0	0	0
18	0	0	0	0	0	0	1	1	-V1
19	1	0	0	0	0	0	1	1	-2V1
20	0	1	0	0	0	0	1	1	-3V1
21	1	1	0	0	0	0	1	1	-4V1
22	0	0	1	0	0	0	1	1	-5V1
23	1	0	1	0	0	0	1	1	-6V1
24	0	1	1	0	0	0	1	1	-7V1
25	1	1	1	0	0	0	1	1	-8V1
26	0	0	0	1	0	0	1	1	-9V1
27	1	0	0	1	0	0	1	1	-10V1
28	0	1	0	1	0	0	1	1	-11V1
29	1	1	0	1	0	0	1	1	-12V1
30	0	0	1	1	0	0	1	1	-13V1
31	1	0	1	1	0	0	1	1	-14V1
32	0	1	1	1	0	0	1	1	-15V1
33	1	1	1	1	0	0	1	1	-16V1

Table 1 Switching sequence for proposed thirty three level inverter

The output waveform has levels: $\pm 16V1, \pm 15V1, \pm 14V1, \pm 13V1, \pm 12V1, \pm 11V1, \pm 10V1, \pm 9V1, \pm 8V1, \pm 7V1, \pm 6V1, \pm 5V1, \pm 4V1, \pm 3V1, \pm 2V1, \pm V1$ and 0.

IV. PROPOSED GRID CONNECTED PV SYSTEM

The proposed PV system directly converts the solar energy into electrical energy in the form of DC. The voltage generated by the PV is converted into AC using the proposed multilevel inverter. The proposed inverter is connected to the power grid with satisfying the grid requirements such as phase angle, frequency, power factor and amplitude of the grid voltage. SPUE: In this attack, an attacker's objective is to maximize its own spectrum usage. When selfish attackers detect a vacant spectrum band, they prevent other secondary users from competing for that band by transmitting signals that emulate the signal characteristics of primary user signals. This attack is mostly carried out by two selfish secondary users. The system is shown in figure 3.

International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 6, June 2016

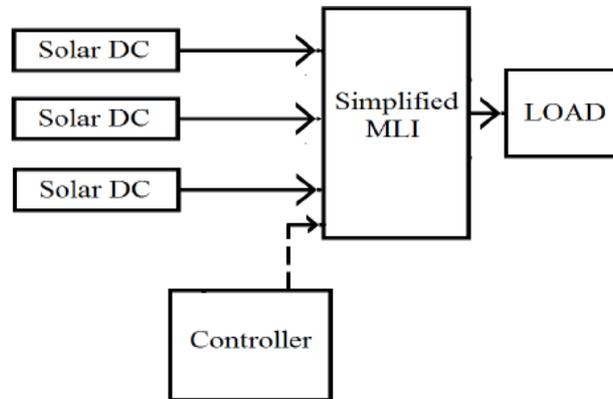


Fig .3 Grid tied photovoltaic system (PV)

V. SIMULATION RESULTS

The Simulation model for the proposed topology is shown in figure 4

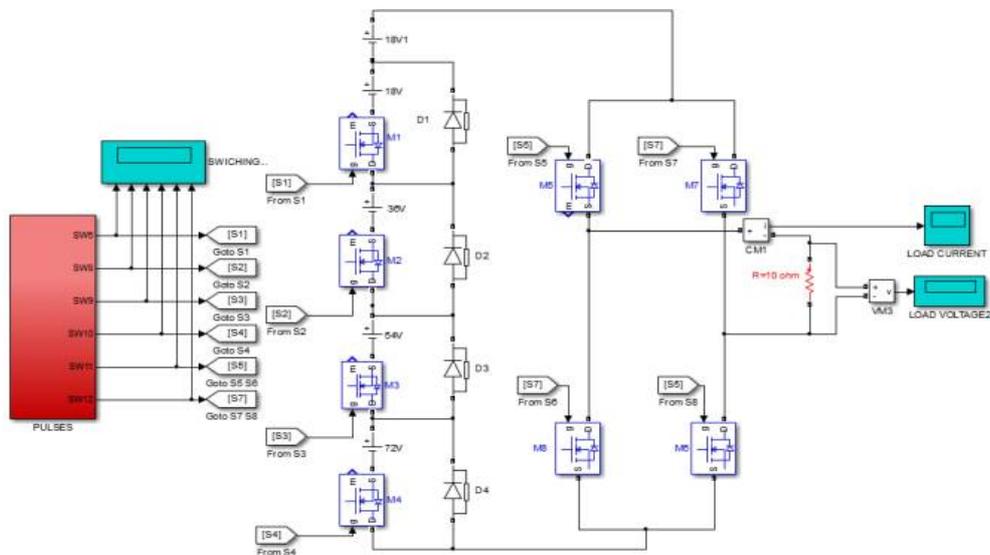


Fig.4. Matlab/Simulink diagram of proposed thirty three level MLI

International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 6, June 2016

The output voltage obtained for thirty three level multilevel inverter using equal area criteria is shown figure 5

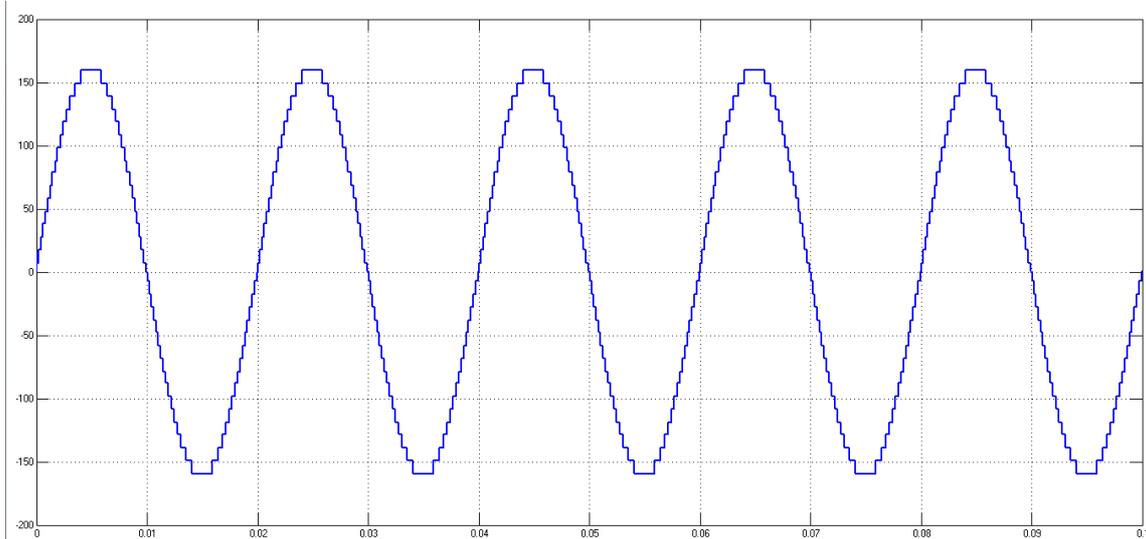


Fig.5. Output voltage wave form of proposed inverter

The figure 6 shows the spectrum analysis (FFT) for proposed multilevel inverter and the THD of the proposed thirty three level inverter is 2.25%.

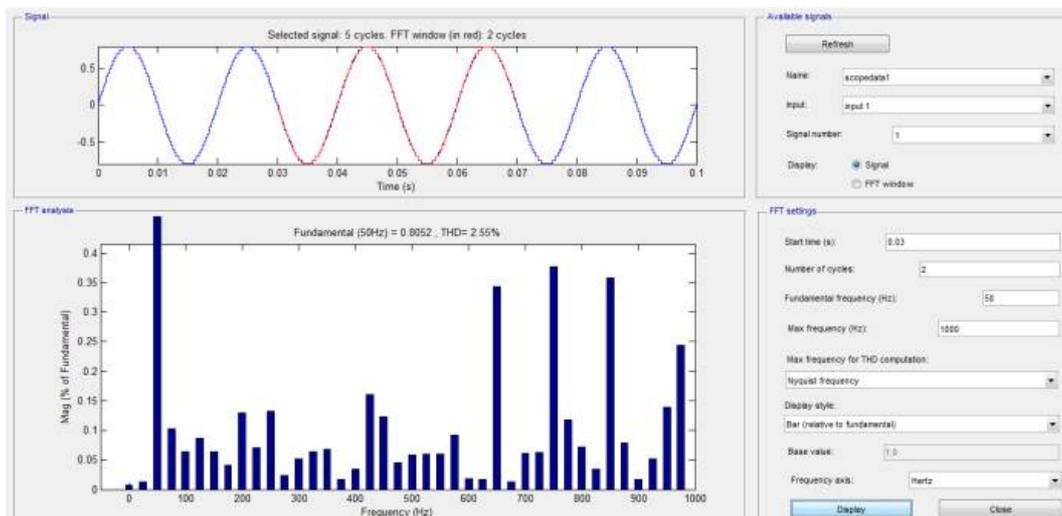


Fig.6. FFT analysis of proposed inverter

The following figure shows the grid voltage and current and it is observed that they are in phase. Hence the power factor is nearly equal to unity.

International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering

(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 6, June 2016

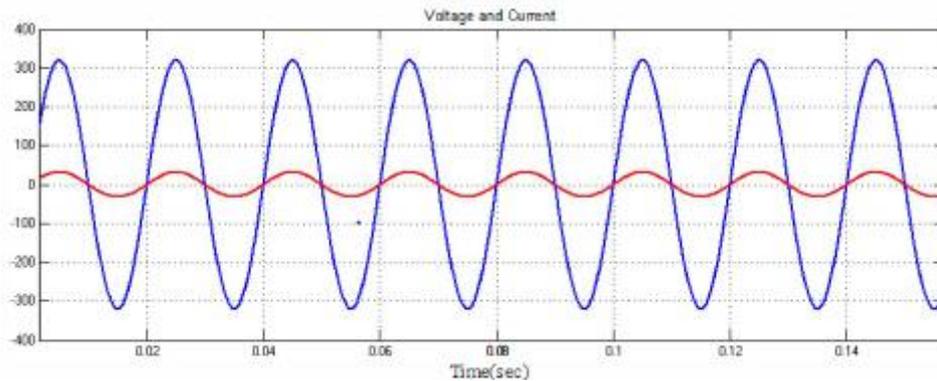


Fig.7. GRID VOLTAGE AND CURRENT

VI.CONCLUSION

This paper proposes a grid connected PV system by using simplified multilevel inverter with less number of switches and the method used for generation pulses is equal area criteria (EAC) is very simple with lower THD as compared to other methods like NR,SHE .Hence the output voltage has lower order harmonics because the filtering is complex.so the voltage and current are in phase

REFERENCES

- [1] Muhammad H Rashid , "Power Electronics: circuits, Devices and Applications", Pearson Education, Third Edition, 2004.
- [2] T. J. Liang, Y. C. Kuo and J. F. Chen. "Single-stage photovoltaic energy conversion system," IEE Proceedings- Electric Power Applications. . Stevenage, vol. 14S, pp. 339-344, July 2001.
- [3] M. Calais, J. Myrzik, T. Spooner, and V. G. Agelidis, "Inverter for singlephase grid connected photovoltaic systems-An overview," in Proc.Power Electron. Spec. Conf., Feb. 2002, vol. 4, pp. 1995-2000..
- [4] J.S. Lai and F.Z. Peng, "Multilevel Converters - A new breed of power converters," Conference Record of the IEEE-IAS Annual Meeting, 1995, pp. 234S-2356.
- [5] M. Manjrekar and G. Venkataramanan, "Advanced topologies and modulation strategies for multilevel inverters," Conference Record of the IEEE-PESC, 1996, pp. 1013-101S.
- [6] J. Rodriguez, J.-S. Lai, and F. Z. Peng, "Multilevel inverters: a survey of topologies, controls, and applications," IEEE Trans. Ind. Electron., vol. 49, pp. 724 -73S, 2002.
- [7] JeyrajSelvaraj and NazrudinA.Rahim, "Multilevel inverter for grid connected pv system employing digital PI controller," IEEE Transactions on Industrial Electronics, vol.56, No1.,January 2009.
- [8] T. Shimizu, O. Hashimoto and G. Kimura, "A novel high performance utility interactive photovoltaic inverter system," IEEE Transactions on Power Electronics. America, vol. IS, pp. 704-711, March 2003.
- [9] V S Prasadrao K, P Sudha Rani and GandhamTabita, " A new multilevel inverter topology for Grid interconnection of PV systems", PESTSE 2014.
- [10] PlemkotaMAHESH andH R Ramesh,"A New Topology of HybridMulti Level Inverter with Equal Area Criteria Switching Technique (EAC)" ijret, june 2016.