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THD Value in a Distribution System Using Seven Level CHB Inverter inside a D-STATCOM

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ABSTRACT: This paper shows the THD value in a distribution system using seven level CHB inverter inside a D-STATCOM. The advantages of CHB inverter are low harmonic distortion, reduced number of switches and suppression of switching losses. The D-STATCOM helps to improve power factor and eliminate THD drawn from a Non Linear Diode Rectifier Load(NLDRL).

KEYWORDS: DSTATCOM, Level shifted Pulse Width Modulation(LSPWM), CHB multilevel inverter.

I.INTRODUCTION

Now a day's power quality is not reliable because of the presence of large number of non linear loads in the distribution system .Industries have to face power quality(PQ) problems like voltage sags and swells. We have two types of PQ compensators .One is shunt connected compensation device and the other is series connected compensation device. The DSTATCOM using multilevel inverter can reduce output harmonics by increasing the number of output voltage levels.

II.LITERATURE SURVEY

There are different multilevel converters such as diode-clamp, flying capacitors and cascaded inverters with separate dc sources. Among all these multilevel converters, cascaded inverters require least number of components. Each level has same structure, and there are no extra clamping diodes or voltage balancing capacitors[2].

III.CASCADED H-BRIDGE INVERTER(CHB)

Among various multilevel inverter topologies cascaded H-Bridge topology is widely used as the voltage levels of output can be increased simply by increasing the number of H-Bridges. Different modulation methods can be applied to CHB. There are two types of CHB topologies. They are symmetrical and asymmetrical CHB.



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IV. SEVEN LEVEL INVERTER BASED D-STATCOM

The schematic diagram of a D-STATCOM is shown in Fig .1

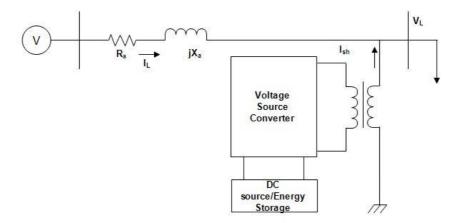


Fig. 1 Schematic diagram of a D-STATCOM

A D-STATCOM consists of a Voltage Source Converter (VSC), a dc energy storage device, a coupling transformer connected in shunt to the distribution network through a coupling transformer. The VSC converts the dc voltage across the storage device into a set of three-phase ac output voltages. These voltages are in phase and coupled with the ac system through the reactance of coupling transformer.

V.PWM TECHNIQUES FOR CHB INVERTER

Here we are adopting Level Shifted Carrier PWM for generating pulses. For a 7-level inverter, there will be three H-Bridges according to the equation L = 2n + 1 where 'L' is the number of levels and 'n' is the number of H-Bridges. So the number of triangular waves is 3.

VI.CIRCUIT DIAGRAM

The circuit diagram of a 7-level CHB inverter is shown in Fig .2

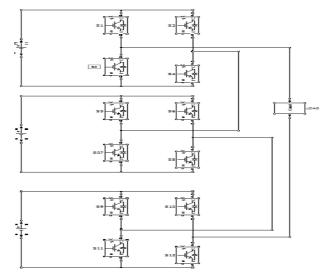


Fig. 2 Seven level CHB inverter



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VII.SWITCHING PATTERN

VOLTAGE LEVELS	S 1	S 2	S 3	<u>S4</u>	S 5	S6	S 7	S 8	S 9	S10	S11	S12
0	0	0	1	1	0	0	1	1	0	0	1	1
V_{dc}	1	0	0	1	0	0	1	1	0	0	1	1
$2V_{dc}$	1	0	0	1	1	0	0	1	0	0	1	1
$3V_{dc}$	1	0	0	1	1	0	0	1	1	0	0	1
0	1	1	0	0	1	1	0	0	1	1	0	0
$-V_{dc}$	0	1	1	0	1	1	0	0	1	1	0	0
$-2V_{dc}$	0	1	1	0	0	1	1	0	1	1	0	0
$-3V_{dc}$	0	1	1	0	0	1	1	0	0	1	1	0

Fig. 3 Switching pattern

VIII.PULSE PATTERN

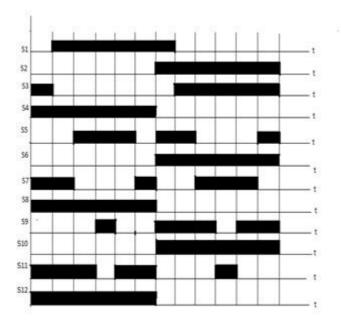


Fig. 4 Pulse pattern



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IX.MATLAB/SIMULINK MODEL AND SIMULATION RESULTS

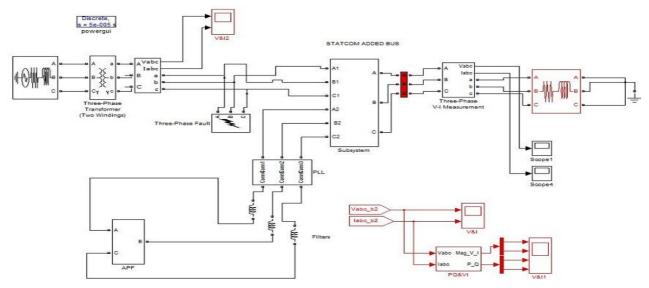


Fig. 5 Simulation diagram

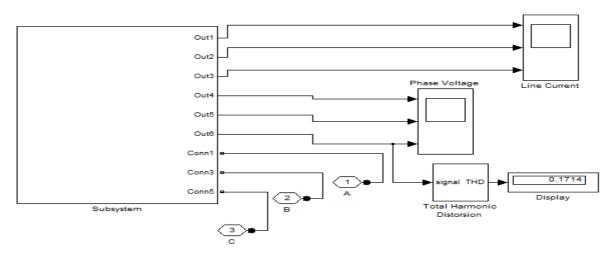


Fig. 6 APF block indicating THD

X.CONCLUSION

The THD value is obtained as 0.1714 as indicated in Fig. 6 . This shows that as voltage level increases, the THD value get reduced.



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