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A New DPC for Three-phase PWM rectifier with unity power factor operation

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ABSTRACT: This paper, presents the performance evaluation for new methods, applied to three-phase PWM rectifier with unity power factor operation, based on virtual flux called Direct Power control (VF-DPC). The principle of this control is based on the instantaneous active and reactive power control loops. The results show the excellent advantages, particularly: nearly sinusoidal input current, regulation of input power factor to unity, low harmonic distortion of line current (THD below 5%), adjustment and stabilization of DC-link voltage (or current), reduced capacitor (or inductor) size due to the continues current.

KEYWORDS: Direct Power Control, Power Factor, PWM Rectifier, Power Quality, THD.

I.INTRODUCTION

In recent decades, the increasing use of non linear loads during these last decades induces severe problems. The problem consists on the distortion in current and voltage wave forms.

Several harmonics cancellation methods were proposed; the typical one was the connection on the ac side of shunt passive filters. However they are bulky, expensive and resonant. Recently, active filtering and PWM conversion (rectification) are taking their places in industry plants.

Development of control methods for active boost rectifiers was possible thanks to advances in power semiconductor devices and digital signal processors, which allow fast operation and cost reduction. It offers possibilities for implementation of sophisticated control algorithms. Appropriate control can provide both the rectifier performance improvements and reduction of passive components which is very important for high power systems.

Three-phase pulse with modulated (PWM) rectifiers (AC/DC converters) has grown rapidly due to some of their important advantages such as: power generation capabilities, control of dc bus voltage, low harmonic distortion of input current and high power factor (usually near unity). In recent years different strategies have been proposed [1] [2] for controlling PWM converter. The main goal of these control strategies is to obtain the high power control and sinusoidal current estimated flux signal is used in the control system.

Therefore their principles are different. One of these methods the voltage oriented control (VOC), guarantees high dynamics and static performance via an internal current control loops; several papers are published in this field. Consequently, the final configuration and performance of this control largely depends on the quality of the applied current control strategy [3]. The classical Direct Power Control (DPC) is based on the instantaneous active and reactive power control loops [4] [5] [6]. In this method, there are no internal current control loops and no PWM modulator block, because the converter switching states are appropriately selected by a switching table based on the instantaneous errors between the commanded and estimated values of active and reactive power. this method requires a good estimate of the active and reactive powers.

This paper covers the existing solution of Direct Power Control and presents a new solution based on Virtual Flux estimation [2]. Theoretical principle of the proposed method is discussed. The steady state and dynamic behaviour of VF-DPC are presented, illustrating the operation and performance of the proposed system as compared with a



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conventional DPC method. This strategy is also investigated under variation of load and DC voltage reference. It is shown that the VF-DPC exhibits several advantages; particularly it provides sinusoidal line current when the supply voltage is non-ideal. Test results show excellent performance of the proposed system.

II.VIRTUAL FLUX ESTIMATION

The voltage imposed by the line power in combination with the AC side inductors are assumed to be quantities related to a virtual AC motor as shown in Fig. 1.

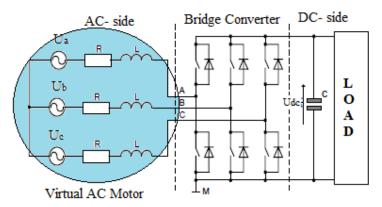


Fig. 1. Three-phase PWM rectifier system with AC-side presented as virtual AC motor

Thus, R and L represent the stator resistance and the stator leakage inductance of the virtual motor and phase-to-phase line voltages: U_{ab} , U_{bc} , U_{ca} would be induced by a virtual air gap flux. In other words the integration of the voltages leads to a virtual line flux vector Ψ_L , in stationary α - β coordinates (Fig.2).

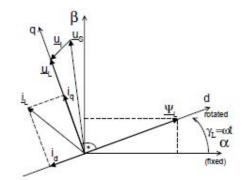


Fig. 2. Reference coordinates and vectors

 Ψ_L -virtual line flux vector, U_S - converter voltage vector, U_L - line voltage vector, U_I - inductance voltage vector, i_L line current vector.

A virtual flux equation can be presented as [7], [8] (Fig.3):

$$\underline{\Psi}_{L(est)} = \underline{\Psi}_s + \underline{\Psi}_I \tag{1}$$



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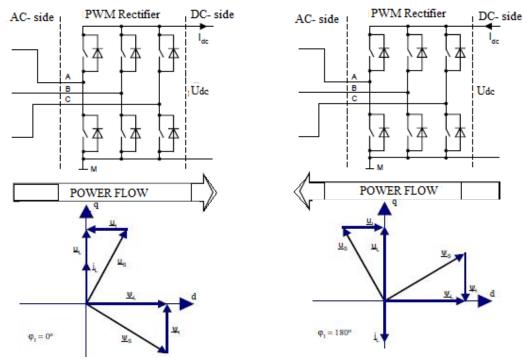


Fig. 3 Relation between voltage and flux for different power flow direction in PWM rectifier

Based on the measured DC-link voltage U_{dc} and the converter switch states S_a , S_b , S_c the rectifier input voltages are estimated as follows:

$$U_{s\alpha} = \sqrt{\frac{2}{3}} U_{dc} (S_a - \frac{1}{2} (S_b + S_c))$$

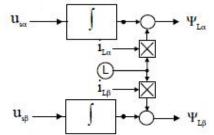
$$U_{s\alpha} = \frac{1}{\sqrt{2}} U_{dc} (S_b - S_c)$$
(2)

Then, the virtual flux Ψ_L components are calculated from the equation (2) in stationary ($\alpha - \beta$) coordinates system.

$$\Psi_{L\alpha(est)} = \int (U_{s\alpha} + L\frac{di_{L\alpha}}{dt})dt$$

$$\Psi_{L\beta(est)} = \int (U_{s\beta} + L\frac{di_{L\beta}}{dt})dt$$
(3)

The virtual flux components calculation is shown in Fig. 4.



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Fig. 6. Block scheme of virtual flux estimator with first order filter



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III.DIRECT POWER CONTROL (DPC)

The main idea of DPC proposed in [10] and next developed by [9] is similar to the well-known Direct Torque Control (DTC) for induction motors. Instead of torque and stator flux the instantaneous active (p) and reactive (q) powers are controlled (Fig.5).

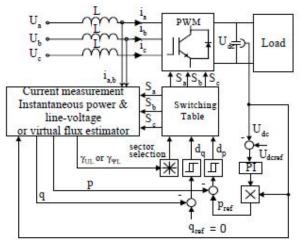


Fig. 5. Block scheme of DPC

The commands of reactive power qref (set to zero for unity power factor) and active power pref (delivered from the outer PI-DC voltage controller) are compared with the estimated q and p values, in reactive and active power hysteresis controllers, respectively.

The digitized output signal of the reactive power controller is defined as:

$$q \prec q_{ref} - H_q \Longrightarrow d_q = 1$$

$$q \succ q_{ref} + H_q \Longrightarrow d_q = 0$$
(4)

and similarly of the active power controller as:

$$p \prec p_{ref} - H_p \Longrightarrow d_p = 1$$

$$p \succ p_{ref} + H_p \Longrightarrow d_p = 0$$
(5)

where: Hq & Hp are the hysteresis bands.

The digitized variables dp, dq and the voltage vector position $\gamma_{UL} = \operatorname{arc} \operatorname{tg} (U_{L\alpha}/U_{L\beta})$ or flux vector position $\gamma_{\Psi L} = \operatorname{arctg} (\psi_{L\alpha}/\psi_{L\beta})$ form a digital word, which by accessing the address of the look-up table selects the appropriate voltage vector according to the switching table (Table I).

S_p	S_q	γ_1	γ_2	γ_3	γ_4	γ_5	γ_6	γ_7	γ_{8}	γ_9	γ_{10}	γ_{11}	γ_{12}
1	0	V_5	V_5	V_6	V_6	V_1	V_1	V_2	V_2	V_3	V_3	V_4	V_4
1	1	V_3	V_3	V_4	V_4	V_5	V_5	V_{6}	V_{6}	V_1	V_1	V_2	V_2
0	0	V_{6}	V_1	V_1	V_2	V_2	V_3	V_3	V_4	V_4	V_5	V_5	V_6
0	1	V_1	V_2	V_2	V_3	V_3	V_4	V_4	V_5	V_5	V_6	V_{6}	V_1

Table .I. Switching table for 12 sectors

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The region of the voltage or flux vector position is divided into twelve sectors, as shown in Fig. 6 and the sectors can be numerically expressed as:

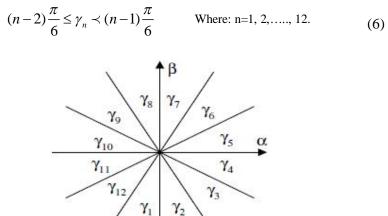


Fig 6. Virtual flux plane with 12 Sectors

Note that the sampling frequency has to be about few times higher than the average switching frequency. This very simple solution allows precisely control of instantaneous active and reactive power and errors are only limited by the hysteresis band. No transformation into rotating coordinates is needed and the equations are easy implemented. This method deals with instantaneous variables, therefore, estimated values contain not only a fundamental but also harmonic components. This feature also improves the total power factor and efficiency [10].

Further improvements regarding VF-DPC operation can be achieved by using sector detection with PLL (Phase-Locked Loop) generator instead of zero crossing voltage detectors (Fig. 7). This guarantees a very stable and free of disturbances sector detection, even under operation with distorted and unbalanced line voltages.

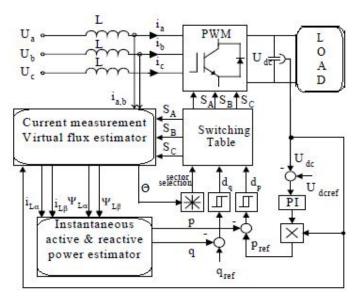


Fig. 7. Block scheme of VF-DPC



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IV.POWER ESTIMATION BASED ON VIRTUAL FLUX

The Virtual Flux (VF) based approach has been proposed by Author to improve the VOC [11], [12]. Here it will be applied for instantaneous power estimation, where voltage imposed by the line power in combination with the AC side inductors are assumed to be quantities related to a virtual AC motor.

With the definitions:

$$\underline{\Psi}_{L} = \int \underline{U}_{L} dt \tag{7}$$

Where:

$$\underline{U}_{L} = \begin{bmatrix} U_{L\alpha} \\ U_{L\beta} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & \frac{1}{2} \\ 0 & \sqrt{3}/2 \end{bmatrix} \begin{bmatrix} U_{ab} \\ U_{bc} \end{bmatrix}$$
(8)

$$\underline{\Psi}_{L} = \begin{bmatrix} \Psi_{L\alpha} \\ \Psi_{L\beta} \end{bmatrix} = \begin{bmatrix} \int U_{L\alpha} dt \\ \int U_{L\beta} dt \end{bmatrix}$$
(9)

$$\underline{i}_{L} = \begin{bmatrix} i_{L\alpha} \\ i_{L\beta} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \frac{3}{2} & 0 \\ \sqrt{3}/2 & \sqrt{3} \end{bmatrix} \begin{bmatrix} i_{\alpha} \\ i_{b} \end{bmatrix}$$
(10)

$$\underline{U}_{S} = U_{con} = \begin{bmatrix} U_{S\alpha} \\ U_{S\beta} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \sqrt{3}/2 & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} U_{AM} \\ U_{BM} \\ U_{CM} \end{bmatrix}$$
(11)

The voltage equation can be written as:

$$\underline{U}_{L} = R\underline{i}_{L} + \frac{d}{dt} \left(L\underline{i}_{L} + \underline{\Psi}_{s} \right)$$
(12)

In practice, R can be neglected, giving:

$$\underline{U}_{L} = L\frac{d\underline{i}_{L}}{dt} + \frac{d}{dt}\underline{\Psi}_{s} = L\frac{d\underline{i}_{L}}{dt} + \underline{U}_{s} \qquad (13)$$

Using complex notation, the instantaneous power can be calculated as follows:

$$p = \operatorname{Re}(\underline{u}_{L}, \underline{i}_{L}^{*})$$

$$q = \operatorname{Im}(\underline{u}_{L}, \underline{i}_{L}^{*})$$
(14)

Where * denotes the conjugate line current vector. The line voltage can be expressed by the virtual flux as:

$$\underline{u}_{L} = \frac{d}{dt} \underline{\Psi}_{L} = \frac{d}{dt} \left(\Psi_{L} e^{j\omega t} \right) = \frac{d\Psi_{L}}{dt} e^{j\omega t} + j\omega \Psi_{L} e^{j\omega t} = \frac{d\Psi_{L}}{dt} e^{j\omega t} + j\omega \underline{\Psi}_{L}$$
(15)

Where $\underline{\Psi}_L$ denotes the space vector and Ψ_L its amplitude. For the virtual flux oriented d-q coordinates (Fig.4), $\underline{\Psi}_L = \Psi_{Ld}$, and the instantaneous active power can be calculated from (14) and (15) as:

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$$p = \frac{d\Psi_{Ld}}{dt}i_{Ld} + \omega\Psi_{Ld}i_{Lq}$$
(16)

For sinusoidal and balanced line voltages, equation (3.19) is reduced to

$$\frac{d\Psi_{Ld}}{dt} = 0$$
(17)
$$p = \omega \Psi_{Ld} i_{Lq}$$

Which means that only the current components orthogonal to the flux $\underline{\Psi}_L$ vector, produce the instantaneous active power. Similarly, the instantaneous reactive power can be calculated as:

$$q = -\frac{d\Psi_{Ld}}{dt}i_{Lq} + \omega\Psi_{Ld}i_{Ld}$$
(18)

and with (17) it is reduced to:

$$q = \omega \Psi_{Ld} i_{Ld} \tag{19}$$

However, to avoid coordinate transformation into d-q coordinates, the power estimator for the DPC system should use stator-oriented quantities, in α - β coordinates (Fig.4).

Using (14) and (15)

$$p = \frac{d\Psi_L}{dt}\Big|_{\alpha} i_{L\alpha} + \frac{d\Psi_L}{dt}\Big|_{\beta} i_{L\beta} + \omega(\Psi_{L\alpha}i_{L\beta} - \Psi_{L\beta}i_{L\alpha})$$

$$q = \frac{d\Psi_L}{dt}\Big|_{\alpha} i_{L\beta} + \frac{d\Psi_L}{dt}\Big|_{\beta} i_{L\alpha} + \omega(\Psi_{L\alpha}i_{L\alpha} + \Psi_{L\beta}i_{L\beta})$$
(20)

For sinusoidal and balanced line voltage the derivatives of the flux amplitudes are zero. The instantaneous active and reactive powers can be computed as [13], [14].

The measured line currents ia, ib and the estimated virtual flux components $\Psi_{L\alpha}$, $\Psi_{L\beta}$ are delivered to the instantaneous power estimator block (PE) as depicted in Fig.8.

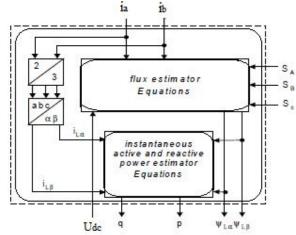


Fig.8. Instantaneous power estimator based on virtual flux



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V. DC VOLTAGE REGULATION

In the proposed DPC scheme, magnitude of fundamental input currents is delivered from the outer proportional-integral (PI) dc-bus voltage controller and will be multiplied by the dc voltage to obtain the reference of the instantaneous active power.

To have unity power factor condition, reference reactive power must be equal to zero. The regulation function is ensured by a PI corrector shown in the figure below:

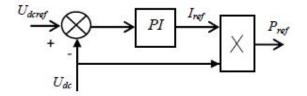


Fig. 9. DC-bus Voltage Controller

Where K_P and K_i are the proportional and integral controller gains respectively. To determine the parameters of the PI controller, we make the following mathematics development. The relationship between the power absorbed by the capacitor and the terminal voltage can be written:

$$P_{ref} = \frac{d}{dt} \left(\frac{1}{2} C U_{dc}^2\right) \tag{21}$$

Based the Laplace transform:

$$U_{dc}^2 = P_{ref} \frac{2}{C.S}$$
(22)

The transfer function of the PI controller can be expressed by:

$$k_p + \frac{k_i}{S} = \frac{1 + \tau S}{T_i S} \tag{23}$$

The transfer function of the closed loop system is given by:

$$F(s) = \frac{\omega_0^2 (1+\tau)}{S^2 + 2\xi \omega_0 S + \omega_0^2}$$
(24)

With:

$$\omega_0 = \sqrt{\frac{2}{CT_i}}$$
 and: $\xi = \frac{\tau}{\sqrt{2CT_i}}$
We found:
 $k_p = \frac{\tau}{T_i}$ and: $k_i = \frac{1}{T_i}$
(25)

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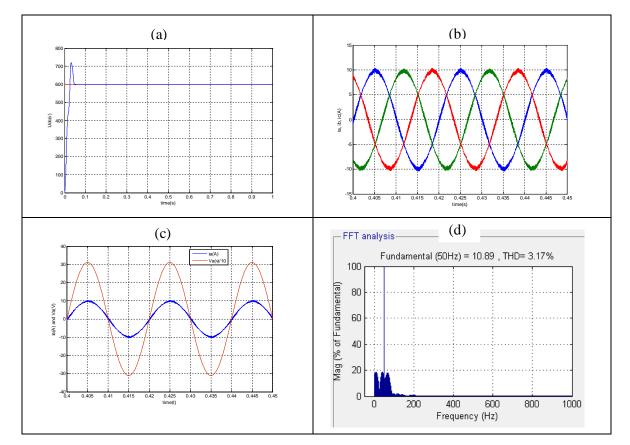
V. RESULTS AND DISCUSSION

To study the operation of the virtual flux based DPC/PWM rectifier, it is implemented in MATLAB/SIMULINK environment. The simulation results obtained for different conditions show in fig 10 to 12. Simulations have been carried out using the main electrical parameters of power circuit and control data showed in Table II.

Resistance of reactors R	0.3Ω
Inductance of reactors L	10mH
dc-bus capacitor C	2mF
Resistance R _{load}	80Ω
Peak amplitude of line voltage	311V
Source voltage frequency	50Hz
DC voltage U _{dc}	600V
Sample time	10 ⁻⁵ s

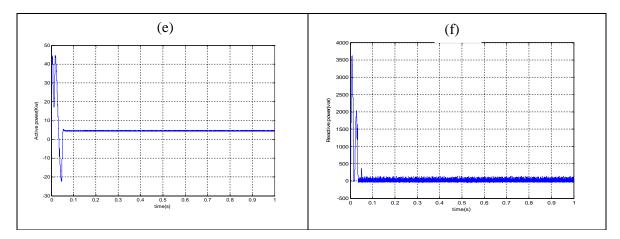
Table II	Electrical	parameters	of nowar	circuit
Table II.	Electrical	parameters	of power	circuit

Several tests were conducted to verity feasibility of the proposed technique. Fig 10 shows the simulated waveforms under unity power factor operation in the steady for purely sinusoidal supply line voltage.





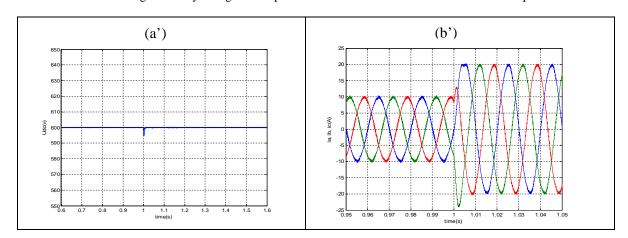
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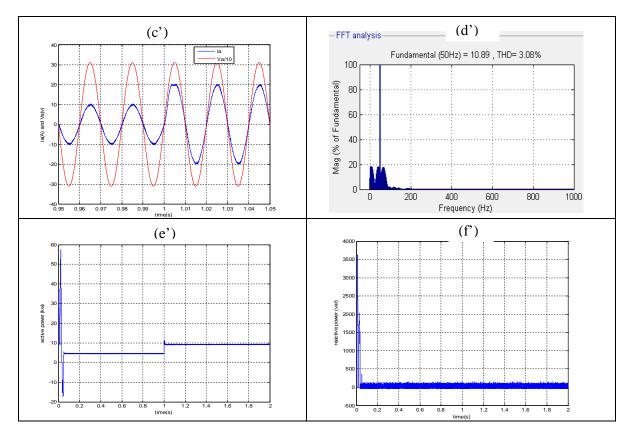
Fig.10. Simulated basic signal waveforms and line current harmonic spectrum under purely sinusoidal line voltage for Udc=600V: From the top: a) output voltage, b) line currents, c) Line current in phase with line voltage d) harmonic spectrum of the line current THD = 3.17%, e) instantaneous active, f) reactive power.

From this figure, it can be seen that the line currents are very close to sine wave (Fig. b) and in phase with the power source voltages (Fig. c) because the reactive power command q_{ref} is set to zero (Fig. f). The active power is constant on average (4.6KW). The reactive power is zero on average because of the unity power factor operation. The effect of dc side load on the operation of system is shown in figure 11. As shown in these figure the change in load don't affect DC link voltage and only change the amplitude of the line current and the line side active power.





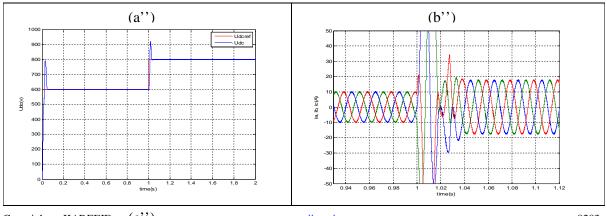
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Fig.11. Simulated basic signal waveforms and line current harmonic spectrum under purely sinusoidal line voltage: From the top: a') output voltage, b') line currents, c') Line current in phase with line voltage d') harmonic spectrum of the line current THD = 3.08%, e') instantaneous active, f') reactive power.

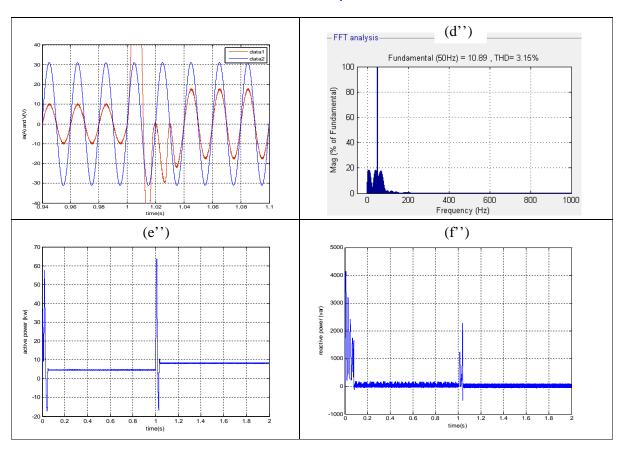
Fig.12 Show that when the DC voltage reaches the new reference value (Fig. a''), the active power and consequently the line current increase (Fig. b''). At the moment of DC step variation reactive power is sensibly reduced. The line voltage and current are almost in phase (fig. c''), and thus the power-factor is almost equal to 1. In the case the wave shape of the line current close to the sinusoid, and hence the THD= 3.15%. In order to maintain the continuous bus charged, the DC voltage variation involves a reference variation in the instantaneous active power.



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Fig.12. Simulated basic signal waveforms and line current harmonic spectrum under purely sinusoidal line voltage: From the top: a') output voltage, b') line currents, c') Line current in phase with line voltage d') harmonic spectrum of the line current THD = 3.08%, e') instantaneous active, f') reactive power.

VI.CONCLUSION

Regarding the advantages of PWM rectifier, in this paper a new control strategy of PWM rectifiers is presented and discuted. To predict behaviour of DPC/PWM rectifier in different load, dynamic model is implemented in matlab/simulink. This method uses the estimated Virtual Flux (VF) vector instead of the line voltage vector. Consequently, voltage sensorless line power estimation is much less noisy thanks to the natural low-pass behaviour of the integrator used in the calculation algorithm. Also, differentiation of the line current is avoided in this scheme. So, the presented VF-DPC of PWM rectifier has the following features and advantages:

- no line voltage sensors are required,
- lower sampling frequency (as conventional DPC),
- sinusoidal line currents (low THD),
- no current regulation loops,
- high dynamic, decoupled active and reactive power control,
- Simple algorithm

It is an important advantage of this converter particularly in speed control drive system that the operation of drive should be robust for line voltage disturbed conditions. As simulation results show, the reactive power is programmable in this system and can be set to obtain unity power factor. Also the lines current have sinusoidal wave form.



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BIOGRAPHY



Jawad Lamterkati was born in Morocco in 1980. He received the Bachelor's and Master's degrees in electrical engineering from the Faculty of Science and Technology in 2005 and the Faculty of Science Semlalia in 2007, Marrakech, Morocco, respectively. He prepares, currently, a Doctorate Thesis in Electrical Engineering Department at the National Higher School of Electricity and Mechanics (ENSEM), Hassan II University, Casablanca, Morocco.



Mohamed Khafallah was born in Morocco in 1964. He received B.Sc., M.Sc. and Doctorate degrees from Hassan II University, Casablanca II, in 1989, 1991 and 1995 respectively, all in Electrical Engineering. In 1995 he joined the Department of Electrical Engineering at the National Higher School of Electricity and Mechanics (ENSEM), Hassan II University, Casablanca. His current research interests are in the application of power electronics converts and motor drives.



Lahcen Ouboubker was born in Morocco in 1982. He received the Bachelor's and Master's degrees in electrical engineering from the Faculty of Science in 2005 and the Faculty of Science Semlalia in 2007, Marrakech, Morocco, respectively. He prepares, currently, a Doctorate Thesis in Electrical Engineering Department at the National Higher School of Electricity and Mechanics (ENSEM), Hassan II University, Casablanca, Morocco.