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Dual Output DC-DC Boost-Cuk Converter

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ABSTRACT: Power converters play an important role in FuelCell applications of vehicles providing the required voltage gain. This paper deals with the new non-isolated DC-DC converters used for such applications. There has been increasing interest in various eco-friendly vehicles such as electric vehicles and plugin hybrid electric vehicles, which have a significant potential to reduce environmental pollution. The converter used combines the main characteristics of both quadratic Boost and Cuk converter having a single active power switch. The voltage stress across the switch is less when compared to other converters. Each Fuel Cell phase is based on the quadratic Boost and Cuk converters(HQBC). They have been designed to offer a very high voltage gain. The high step-up DC-DC converter for fuel cell vehicles based on merged quadratic boost-cuk converter is simulated in MATLAB/SIMULINK version 2017a. The efficiency of the converter is about 80% in the DC-DC boost-cuk converter. This converter give a high voltage gain with small voltage ripple when compared to quadratic DC-DC boost converter and can be used to get dual output which can be used for fuel cell applications.

KEYWORDS: Fuel cell electric vehicles, single switch, Boost and Cuk converter, Dual output.

I.INTRODUCTION

The transportation sector plays a vital role for modern society. Lot of modified vehicles are emerging day by day with more efficiency and performance. Given its importance it is almost impossible to globally reduce the number of transportation vehicles on the roads, oceans and skies despite the environmental problems created by their massive use. To reduce critical levels of pollution, especially in big cities, many countries have adopted restricted policies to the use of internal combustion engines[1]. Electric vehicles (EVs) have emerged in the recent years as a feasible alternative to internal combustion engine (ICE) vehicles aiming to reduce the high petroleum dependency and to reduce pollution which is the major threat that we are facing today. Electric vehicle is more efficient, environment friendly, and cleaner than the vehicle that relies on fossile fuels, especially when smart grids have become omnipresent[2]. Electric vehicle research has been mainly focused on the design and optimization of energy storage devices(ESDs), power electronics for energy conversion on-board, design of powertrain component types and sizes[1], grid charging management[2], among other aspects.

A hybrid electric drive train utilizes two or more energy sources for propulsion. In a typical HEV drive train, the internal combustion engine and a secondary energy storage device or power source, such as battery, fuel cell, or ultracapacitors, provide traction power[4]. Power converters play an important role in Fuel-Cell applications. Conventional DC-DC Boost is unsuitable because it have some limitations such as low efficiency at high duty cycle ratio, high frequency losses, reverse recovery problems. To overcome these problems several DC-DC Boost topologies have been proposed to increase the voltage level of the FC[1].

DC-DC Boost converters may donot provide wide voltage gain, low voltage stress for power switches and reduced input current ripple at the same time. So to reduce input high step-up Boost converters with quadratic gain characteristics in combine with Cuk converter with dual output are proposed. They have been designed to oer a very high voltage gain and can used for the application of two different voltage levels at a time.

II.CIRCUIT CONFIGURATION

Quadratic Boost DC-DC converters have been used and studied due to their high voltage gain characteristics. This converter is characterized by a simple structure using a single active switch (Fig. 1). The voltage stress that the active switch must withstand is lower than that of the output voltages of the converter. The topology of the converter is obtained by merging a quadratic Boost with Cuk converter. The proposed quadratic DC-DC Boost cuk (HQBC)converter has dual output which can be used for fuel cell applications or for any other applications.

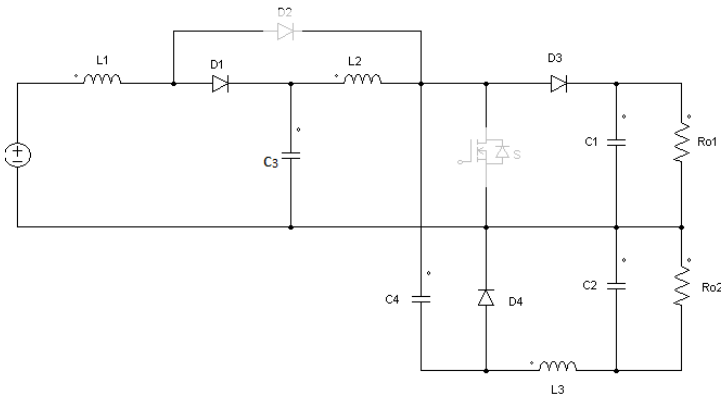


Fig. 1. Dual Output DC-DC Boost Cuk converter

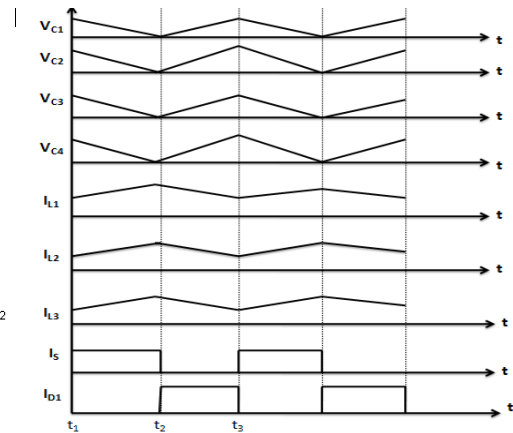


Fig. 2. Theoretical Waveform

III. PRINCIPLE OF OPERATION

The principle of operation of the converter is as shown in figure 2. The switching period of the dual output converter is divided into two time intervals and have two modes of operations.

1) Interval [t1 -t2] (corresponding to fig.3): In the instant t1 the switch S turns on starting the 1st stage and finishing the previous transient process. The energy from the DC source and capacitors C3, C2 and C1 starts to be transferred to inductors, L1, L2 and L3, increasing their respective currents. Diodes D1, D3 and D4 will be turned off and D2 is turned on.

2) Interval [t2 -t3] (corresponding to fig.4): This subinterval is associated to the 2nd stage and started with the turn-off of switch S. The energy stored in the inductors L1, L2 and L3 will be transferred to capacitors C3, C1 and C2, decreasing the inductor currents.

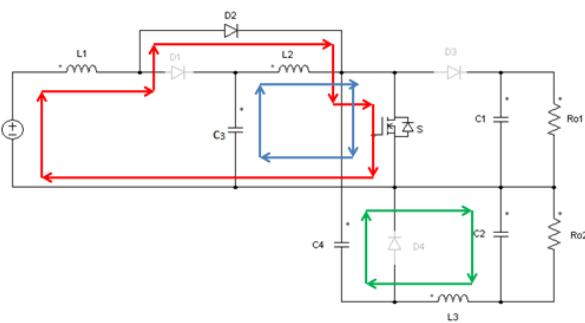


Fig. 3. Mode1 operation of dual output converter

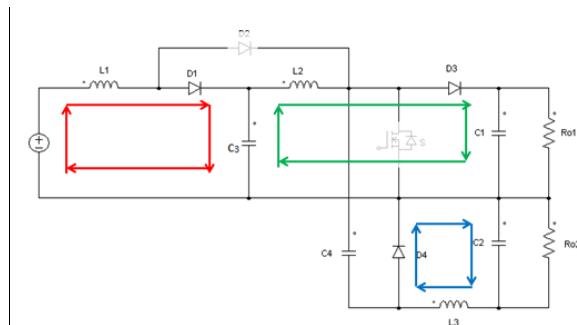


Fig. 4. Mode2 operation of dual output converter

IV. SIZING OF PASSIVE COMPONENTS

The passive elements (inductors and capacitors) are sized according to a given limit for their state variable ripple. For inductors the maximum current ripple (ΔI_L) will be taken as a percentage of the inductor average current and voltage ripple (ΔV_O) is taken as a percentage of the output voltage V_O .

$$V_O / V_i = (1 + D) / (1 - D)^2 \quad \text{----- (1)}$$

D get its maximum value at 0.5. So $D_{\text{boundary}} = 0.5$. ΔI_L is taken as 20 to 40% of I_O .

$$L_1 = D_{\text{boundary}} * (V_O / (\Delta I_{L1} * f)) \quad \text{----- (2)}$$

The values for all the three inductors used in this dual output converter are same Taken ΔV_O as 1 to 5% of V_O and here ΔV_{C1} and ΔV_{C3} is taken as 4% of V_O , ΔV_{C4} is taken as 2% of V_O .

$$C_1 = (D * I_O) / (\Delta V_{C1} * f) \quad \text{----- (3)}$$



Here the values of capacitors C_1 and C_2 are taken to be same.

$$C_3 = (D \cdot I_O) / ((1-D)^2 \cdot \Delta V_{C3} \cdot f) \quad \text{-----(4)}$$

$$C_4 = (D \cdot I_O) / (\Delta V_{C4} \cdot f) \quad \text{-----(5)}$$

V. SIMULATION RESULTS

The prototype of dual output quadratic DC-DC Boost Cuk converter have nominal power of 160W, 24V input voltage, inductors $L_1=10\text{mH}$, $L_2=10\text{mH}$ and $L_3=10\text{mH}$ and Capacitors $C_1=C_2=100\mu\text{F}$, $C_3= 40\mu\text{F}$ and $C_4= 20\mu\text{F}$. To avoid human audible noise we use a switching frequency of 20kHz. Power semiconductors selected were the UJC06505K for the MOSFETs, the SCS240AE2HR for the diodes D1 and D1 and the VS-60EPU04PbF for the diodes D1 and D1. The dual output for this converter is obtained with a duty cycle of 0.6 conforming the expected gain of this converter.

A. SIMULATION PARAMETERS

Parameters	Specification
Input voltage (V_{in})	24V
Switching frequency (f_s)	20KHz
Load resistance($R_{01}=R_{02}$)	160Ω
Capacitor ($C_1= C_2, C_3, C_4$)	100μF,40μF,20μF
Inductor($L_1 = L_2= L_3$)	10mH

Simulation results are obtained by using MATLAB. MATLAB is a high-performance language for technical computing. It integrates computation, visualization, and programming in an easy way to use environment where problems and solutions are expressed in familiar mathematical notation. SIMULINK is a software package for modelling, simulating, and analysing dynamical systems.

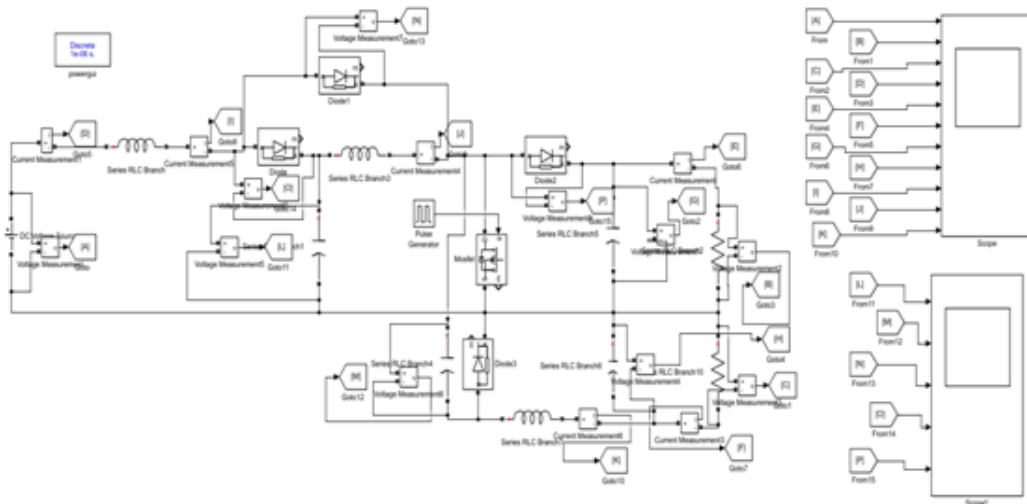


Fig.5. Simulink Model Of Dual Output DC-DC Boost-Cuk Converter

The dual output quadratic DC-DC Boost-Cuk converter is simulated in MATLAB/SIMULINK by choosing the parameters listed in the Table and the Simulink model is shown in figure 5.

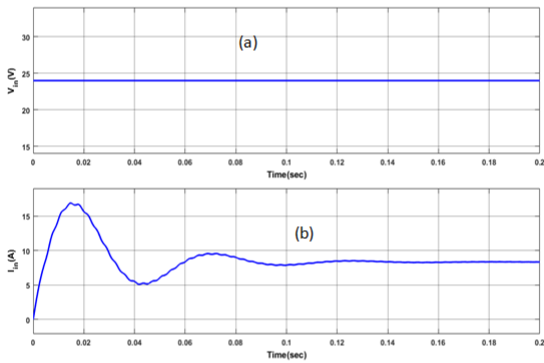


Fig. 6. (a) Input Voltage (V_{IN}) (b) Input current (I_{IN})

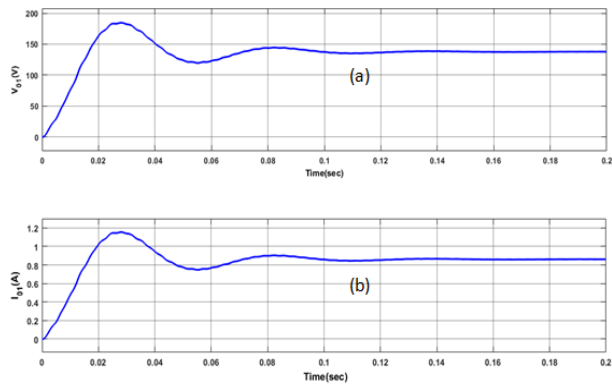


Fig. 7. (a) Output Voltage (V_{O1}) (b) Output current (I_{O1})

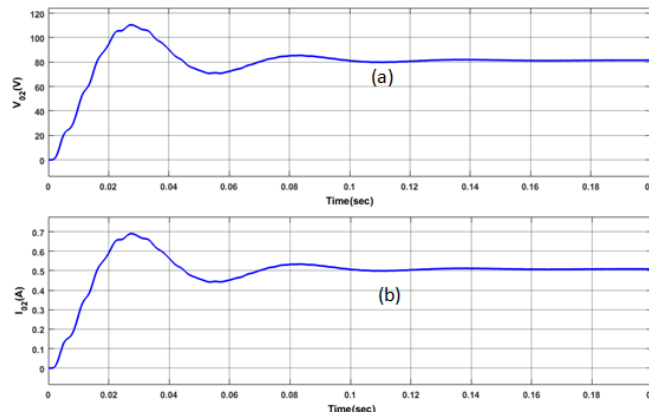


Fig. 8. (a) Output Voltage (V_{O2}) (b) Output current (I_{O2})

From the figure 6, it can be seen that the input voltage V_{IN} is 24 V and the switching frequency is chosen to be 20 kHz and the duty ratio of S to 0.6. The input current obtained have the value of 8.3A with a small current ripple of 0.06A. Figure 7 and 8 shows the dual output obtained. The first output voltage obtained is 137V with a voltage ripple of 0.3V and its corresponding output current is 0.86A with a ripple of 0.002A. The second output voltage obtained is 81.45V with a voltage ripple of 0.3V and its output current is 0.509A with a ripple of 0.001A.

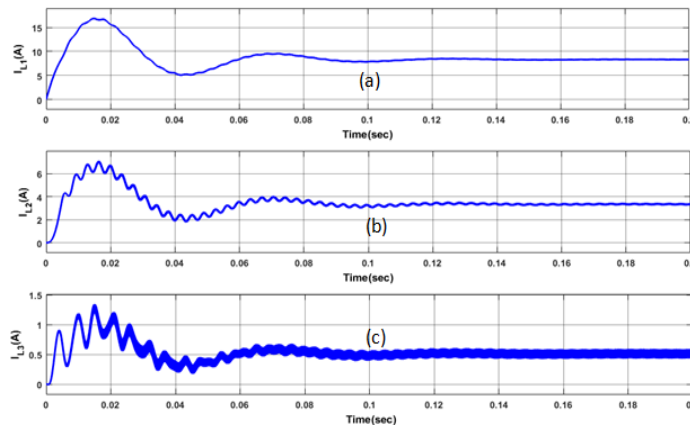


Fig. 9. Inductor Currents (a) L_1 (b) L_2 and (c) L_3



From the Figure 9, the inductor current of $L_1=8.3A$ with a ripple of 0.05A, $L_2=3.3A$ with a ripple of 0.15A and $L_3=0.5A$ with a ripple of 0.1A is shown respectively.

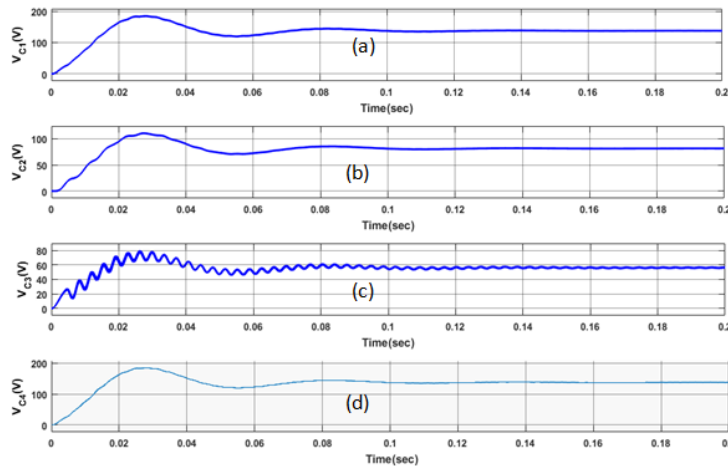


Fig. 10. Voltage across(a) V_{C1} (b) V_{C2} (c) V_{C3} and (d) V_{C4}

Voltage across the capacitors V_{C1} is 137V with a ripple of 0.3V , V_{C2} is 81.45V with a ripple of 0.03V, V_{C3} is 55V with a ripple of 2V and V_{C4} is 137V with a ripple of 0.02V.

VI. ANALYSIS

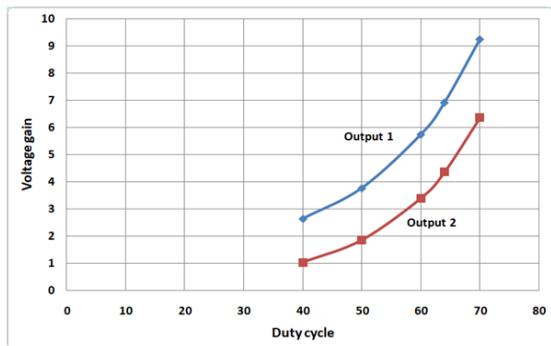


Fig. 12. Analysis of voltage gain Vs Duty cycle

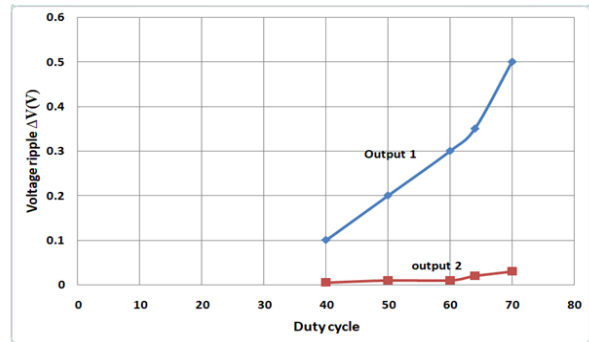


Fig. 13. Analysis of voltage ripple Vs Duty cycle

The above figure 12 shows the analysis of voltage gain with duty ratio of converter. From the figure it is seen that the voltage gain of output 1 is higher than that of output 2. The maximum voltage gain obtained is 9.2 for output 1 and 6.3 for output 2 of the dual converter. As the value of duty cycle increases the voltage ripple for corresponding converter increases and it can be seen from figure 13.

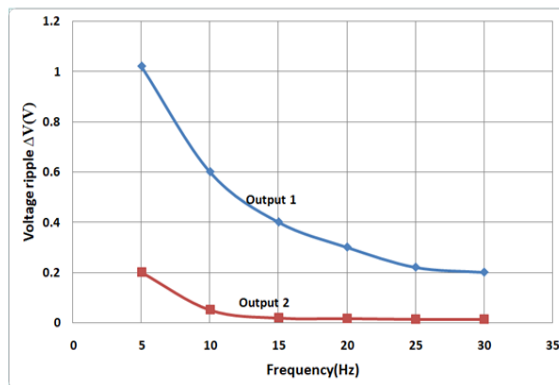


Fig. 14. Analysis of voltage ripple Vs Frequency

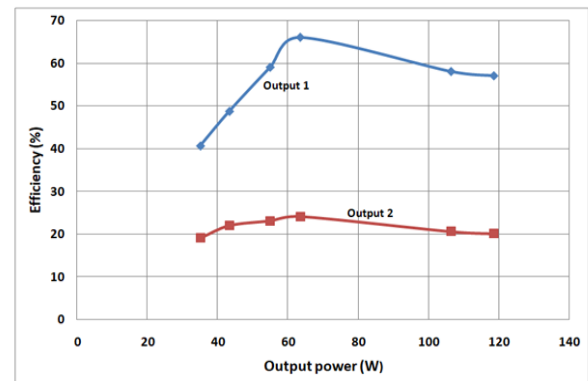


Fig. 15. Analysis of efficiency Vs output power

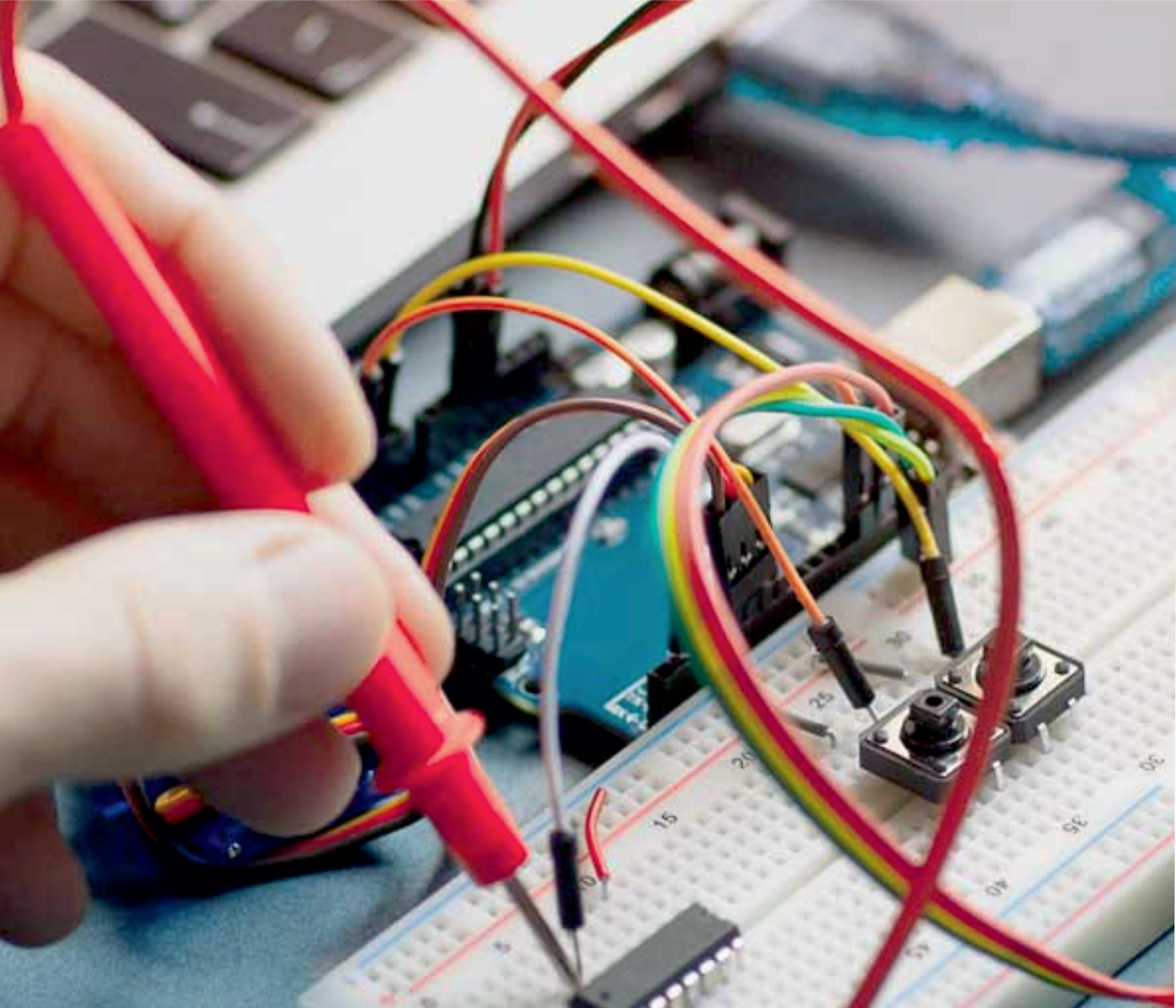
It is clear that the voltage ripple of dual converter decreases with increase in frequency. As frequency increases voltage ripple of the converters decreases from a high value as shown in figure 14. As in the above figure 15, the efficiency increases with increase in output power and reaches its maximum value and then it starts decreasing for the dual outputs of the converter. These all are the output simulation results obtained from the dual output DC-DC Boost Cuk converter.

V. CONCLUSION

The Dual Output DC-DC Boost-Cuk converter were obtained by integrating both a quadratic Boost and a Cuk converter around a single active power switch. This converter can be used for dual purpose or applications of different voltage levels. The dual output DC-DC HQBC converters are characterized by increased voltage gains compared to the quadratic Boost converter. The maximum efficiency is 80% for an input voltage of 24V in this converter. This can be used for fuel cell applications and since it have dual output of different voltages. We can use it for two applications at a time.

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