

Power-Quality Improvement with D-STATCOM Based Voltage Controller for PMSM Drive

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ABSTRACT: Power Quality (PQ) has become an important issue since many Non-Linear loads at various distribution ends have become intolerant to harmonic content. Power Quality (PQ) mainly deals with issues like maintaining a fundamental supply voltage and Load current at the Point of Common Coupling (PCC) for various distribution voltages. The basic operation principle of the DSTATCOM is to inject an appropriate current in parallel with the supply. The VSC connected in shunt with the ac system provides a multifunctional topology which can be used for up to three quite distinct purposes like, voltage regulation and compensation of reactive power, correction of power factor and elimination of current harmonics.

In this paper a latest algorithm to make reference voltage used for a distribution static compensator (DSTATCOM) working in voltage-control method. The proposed method ensures that unity power factor (UPF) is reached at the load end for the duration of nominal process, which is not achievable in the conventional method. Also, the compensator inserts lower currents and thus, decreases losses in the feeder and voltage source inverter. In this thesis, Power quality circuit is developed with voltage source converter in the Permanent Magnet Synchronous Motor drive. The organize of PMSM drive used is Field Oriented Control by means of pulse width Modulation. The power quality circuit planned in this work increases the power factor and decreases the harmonic distortion. Simulation work is performed by using MATLAB/ SIMULINK software.

KEYWORDS: PQ; PMSM; D-STATCOM;

I. INTRODUCTION

A Distribution system suffers from current as well as voltage-related power-quality (PQ) problems, which include poor power factor, distorted source current, and voltage disturbances [1], [2]. A DSTATCOM, connected at the point of common coupling (PCC), has been utilized to mitigate both types of PQ problems [2]–[12]. When operating in current control mode (CCM), it injects reactive and harmonic components of load currents to make source currents balanced, sinusoidal, and in phase with the PCC voltages [3]–[7]. In voltage-control mode (VCM) [2], [8]–[12], the DSTATCOM regulates PCC voltage at a reference value to protect critical loads from voltage disturbances, such as sag, swell, and unbalances. However, the advantages of CCM and VCM cannot be achieved simultaneously with one active filter device, since two modes are independent of each other.

In CCM operation, the DSTATCOM cannot compensate for voltage disturbances. Hence, CCM operation of DSTATCOM is not useful under voltage disturbances, which is a major disadvantage of this mode of operation [13]. Traditionally, in VCM operation, the DSTATCOM regulates the PCC voltage at 1.0 p.u. [2], [8]–[11]. However, a load works satisfactorily for a permissible voltage range [14]. Hence, it is not necessary to regulate the PCC voltage at 1.0 p.u. While maintaining 1.0-p.u. voltage, DSTATCOM compensates for the voltage drop in feeder. For this, the compensator has to supply additional reactive currents which increases the source currents. This increases losses in the voltage-source inverter (VSI) and feeder. Another important aspect is the rating of the VSI. Due to increased current injection, the VSI is de-rated in steady-state condition. Consequently, its capability to mitigate deep voltage sag decreases. Also, UPF cannot be achieved when the PCC voltage is 1 p.u. In the literature, so far, the operation of DSTATCOM is not reported where the advantages of both modes are achieved based on load requirements while overcoming their demerits.

This paper considers the operation of DSTATCOM in VCM and proposes a control algorithm to obtain the reference load terminal voltage. This algorithm provides the combined advantages of CCM and VCM. The UPF operation at the

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PCC is achieved at nominal load, whereas fast voltage regulation is provided during voltage disturbances. Also, the reactive and harmonic component of load current is supplied by the compensator at any time of operation. The deadbeat predictive controller [15]–[17] is used to generate switching pulses. The control strategy is tested with a three-phase four-wire distribution system. The effectiveness of the proposed algorithm is validated through detailed simulation and experimental results

II. PROPOSED CONTROL SCHEME

Circuit diagram of a DSTATCOM-compensated distribution system is shown in Fig. 1. It uses a three-phase, four-wire, two-level, neutral-point-clamped VSI. This structure allows independent control to each leg of the VSI [7]. Fig. 2 shows the single-phase equivalent representation of Fig. 1. Variable v_i is a switching function, and can be either 0 or V_{dc} depending upon switching state. Filter inductance and resistance are L_f and R_f respectively. Shunt capacitor eliminates high-switching frequency components.

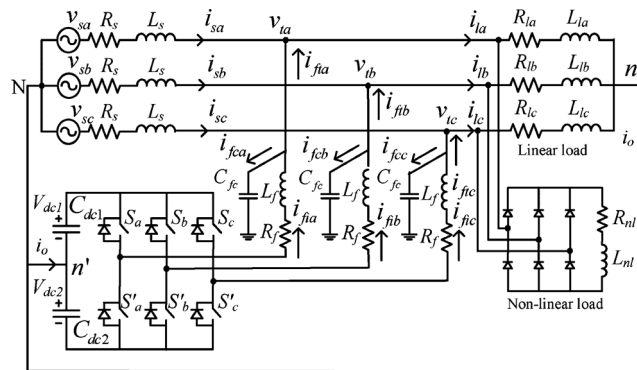


Fig. 1. Circuit diagram of the DSTATCOM-compensated distribution system

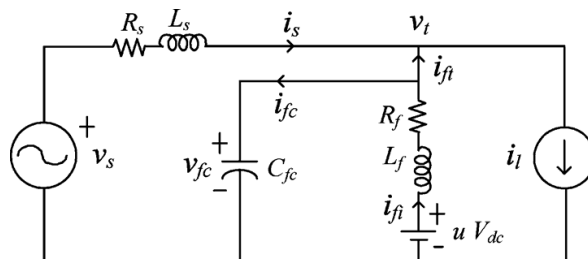


Fig. 2. Single-phase equivalent circuit of DSTATCOM.

The instantaneous symmetrical component theory and complex Fourier transform, a reference voltage magnitude generation scheme is proposed that provides the advantages of CCM at nominal load. The overall controller block diagram is shown in Fig. 3

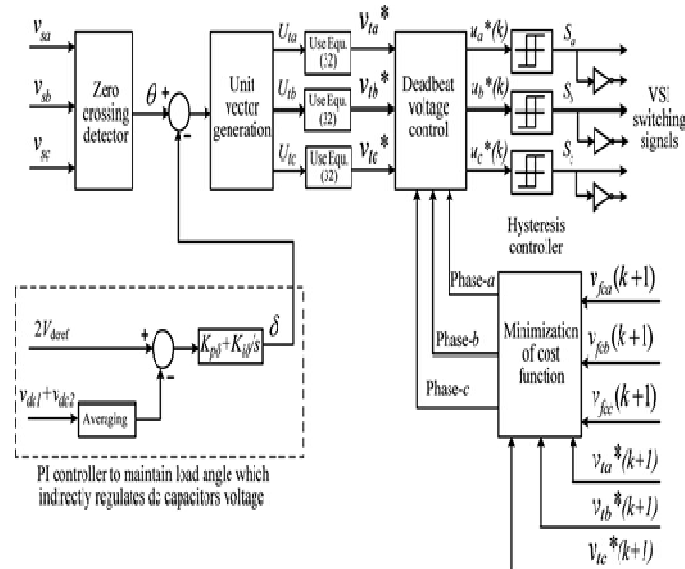


Fig. 3. Overall block diagram of the controller to control DSTATCOM in a distribution system

III. PERMANENT MAGNET SYNCHRONOUS MACHINE

Under steady state condition is proportional to the frequency of the current in its armature. Fig. 4 indicates the PMSM Cylindrical rotor and Salient rotor structures. The magnetic field created by the armature currents rotates at the same speed as that created by the field current on the rotor, which is rotating at the synchronous speed, and a steady torque results. Synchronous machines are commonly used as generators especially for large power systems, such as turbine generators and hydroelectric generators in the grid power supply. Because the rotor speed is proportional to the frequency of excitation, synchronous motors can be used in situations where constant speed drive is required. Since the reactive power generated by a synchronous machine can be adjusted by controlling the magnitude of the rotor field current, unloaded synchronous machines are also often installed in power systems solely for power factor correction. The armature winding of a conventional synchronous machine is almost invariably on the stator and is usually a three phase winding. The field winding is usually on rotor and excited by dc current, or permanent magnets. The dc power supply required for excitation usually is supplied through a dc generator known as exciter, machine which is often mounted on the same shaft as the synchronous.

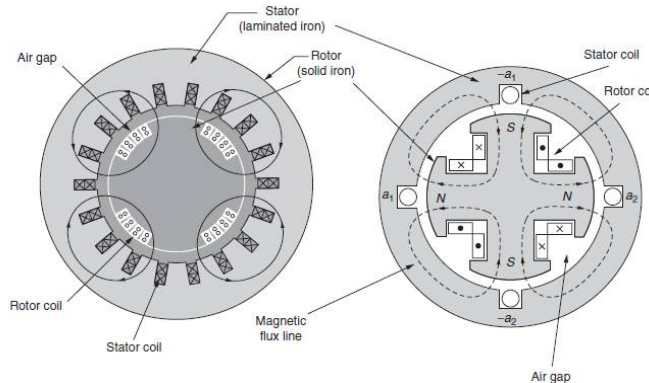


Fig.4. Cylindrical rotor and Salient rotor structures.

A. PHOTOVOLTAIC SYSTEM

A photovoltaic system, converts the light received from the sun into electric energy. In this system, semi conductive materials are used in the construction of solar cells, which transform the self-contained energy of photons into electricity, when they are exposed to sun light. The cells are placed in an array that is either fixed or moving to keep

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tracking the sun in order to generate the maximum power [9]. These systems are environmental friendly without any kind of emission, easy to use, with simple designs and it does not require any other fuel than solar light. On the other hand, they need large spaces and the initial cost is high. PV array are formed by combine no of solar cell in series and in parallel. A simple solar cell equivalent circuit model is shown in figure. To enhance the performance or rating no of cell are combine. Solar cell are connected in series to provide greater output voltage and combined in parallel to increase the current. Hence a particular PV array is the combination of several PV module connected in series and parallel. A module is the combination of no of solar cells connected in series and parallel.

The photovoltaic system converts sunlight directly to electricity without having any disastrous effect on our environment. The basic segment of PV array is PV cell, which is just a simple p-n junction device. The fig.5 manifests the equivalent circuit of PV cell. Equivalent circuit has a current source (photocurrent), a diode parallel to it, a resistor in series describing an internal resistance to the flow of current and a shunt resistance which expresses a leakage current. The current supplied to the load can be given as.

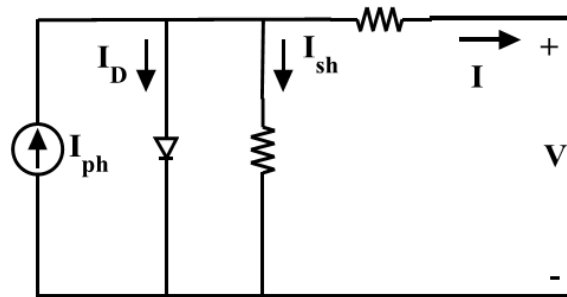


Fig.5. Equivalent circuit of Single diode modal of a solar cell..

IV. MATLAB/SIMULINK RESULTS

Simulation results of this paper is as shown in bellow Figs.8 to 19.

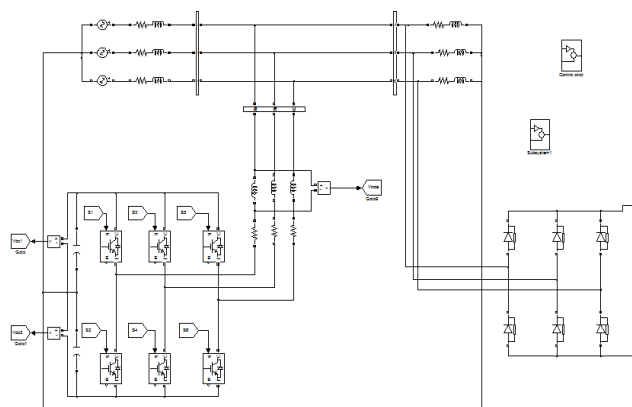


Fig.8.Simulation results for Conventional/Proposed system.

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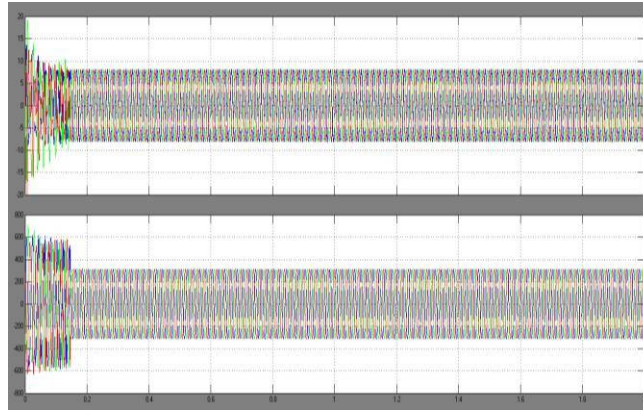


Fig.8.Source voltage and current.



Fig.9.dc link voltage.

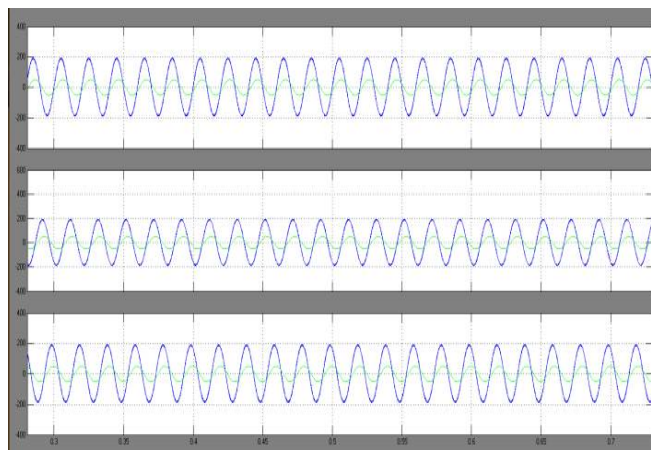


Fig.10. Terminal voltages and source currents using the traditional method. (a) Phase-A. (b) Phase-B. (c) Phase- C.

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Fig. 11. Phase- source RMS currents.

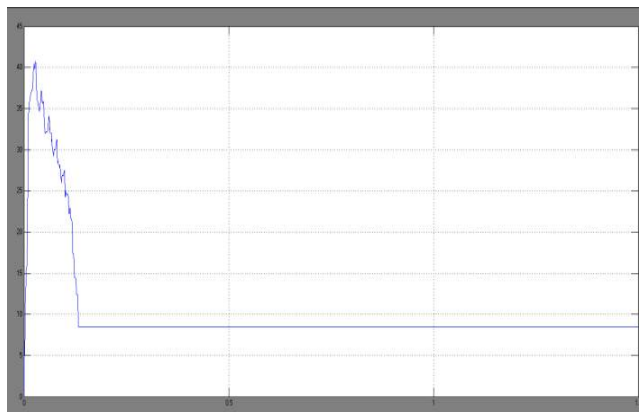


Fig. 12. Phase- compensator RMS currents.

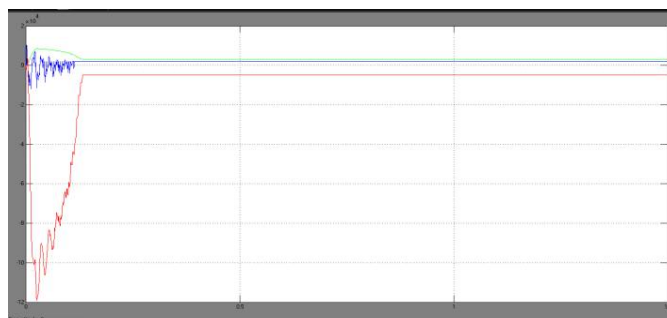


Fig.13. Load reactive power (Q-Load), compensator reactive power (Q-VSI), and reactive power at PCC (Q-PCC).

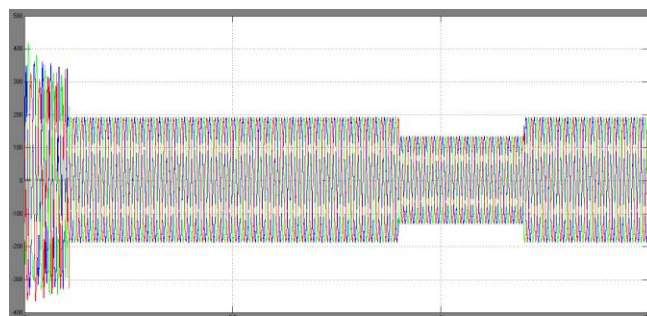


Fig.14. Simulation results for Source Voltage during Sag.

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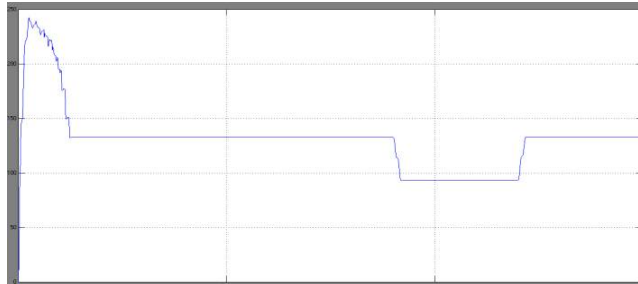


Fig.15. simulation result for RMS value of source voltage during sag.

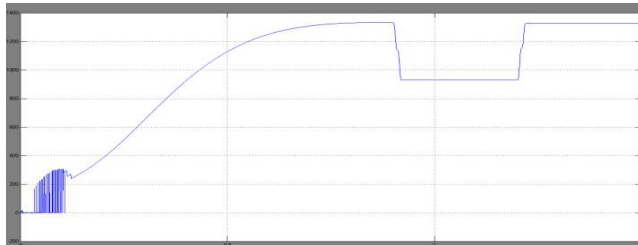


Fig.16.Simulation results for dc link voltage

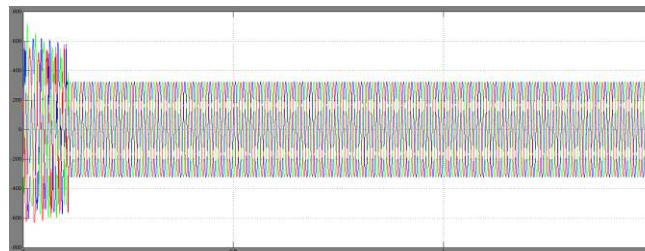


Fig.17. Simulation results for load voltage after compensation

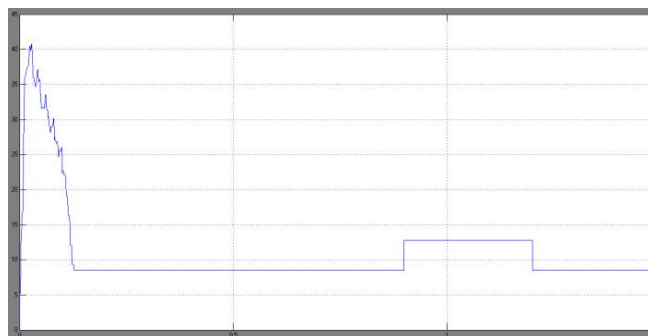


Fig.17. simulation results for compensation current of D- statcom

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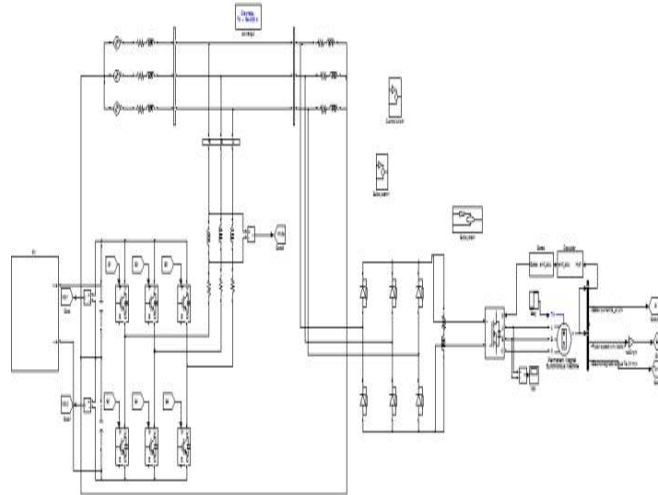


Fig.18. Simulink circuit for conventional D-statcom with PV & PMSM Drive.

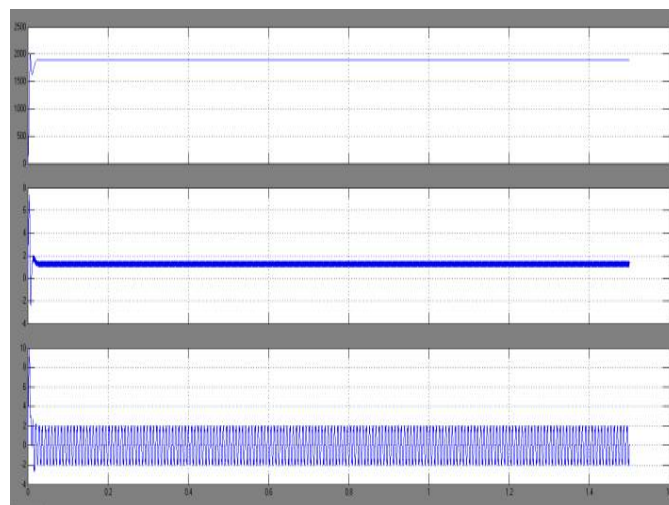


Fig.19. speed, torque and armature current of PMSM drive

V. CONCLUSION

In this paper, a control algorithm has been proposed for the Generation of reference load voltage for a voltage-controlled DSTATCOM. The performance of the proposed scheme is compared with the traditional voltage-controlled DSTATCOM. The proposed method provides the following advantages:

- At nominal load, the compensator injects reactive and harmonic components of load currents, resulting in UPF;
- Nearly UPF is maintained for a load change;
- Fast voltage regulation has been achieved during voltage disturbances

The SVM inverter increases the output voltage and lowers the output harmonic distortions compared with the conventional sinusoidal PWM inverter. The field oriented control using space vector modulation allows easy implementation of the PMSM drive with fewer harmonic. The advantages of the proposed drive are confirmed by the simulation results.



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REFERENCES

- [1] M. H. J. Bollen, *Understanding Power Quality Problems—Voltage Sags and Interruptions*. New York, NY, USA: IEEE Press, 2000.
- [2] A. Ghosh and G. Ledwich, *Power Quality Enhancement Using Custom Power Devices*. London, U.K.: Kluwer, 2002.
- [3] M. H. J. Bollen and I. Gu, *Signal Processing of Power Quality Disturbances*. Hoboken, NJ, USA: Wiley-IEEE Press, 2006.
- [4] R. C. Dugan, M. F. McGranaghan, and H. W. Beaty, *Electric Power Systems Quality*, 2nd ed. New York, NY, USA: McGraw-Hill, 2006.
- [5] A. Moreno-Munoz, *Power Quality: Mitigation Technologies in a Distributed Environment*. London, U.K.: Springer-Verlag, 2007.
- [6] K. R. Padiyar, *FACTS Controllers in Transmission and Distribution*. New Delhi, India: New Age Int., 2007. IEEE Recommended Practices and Recommendations for Harmonics Control in Electric Power Systems, IEEE Std. 519, 1992.
- [7] V. B. Bhavraj and P. N. Enjeti, "An active line conditioner to balance voltages in a three phase system," *IEEE Trans. Ind. Appl.*, vol. 32, no. 2, pp. 287–292, Mar./Apr. 1996.
- [8] S. Middlekauff and E. Collins, "System and customer impact," *IEEE Trans. Power Del.*, vol. 13, no. 1, pp. 278–282, Jan. 1998.
- [9] M. Vilathgamuwa, R. Perera, S. Choi, and K. Tseng, "Control of energy optimized dynamic voltage restorer," in *Proc. IEEE IECON*, 1999, vol. 2, pp. 873–878.
- [10] J. G. Nielsen, F. Blaabjerg, and N. Mohan, "Control strategies for dynamic voltage restorer compensating voltage sags with phase jump," in *Proc. IEEE APEC*, 2001, vol. 2, pp. 1267–1273.
- [11] A. Ghosh and G. Ledwich, "Compensation of distribution system voltage using DVR," *IEEE Trans. Power Del.*, vol. 17, no. 4, pp. 1030–1036, Oct. 2002.
- [12] A. Ghosh and A. Joshi, "A new algorithm for the generation of reference voltages of a DVR using the method of instantaneous symmetrical components," *IEEE Power Eng. Rev.*, vol. 22, no. 1, pp. 63–65, Jan. 2002.
- [13] I.-Y. Chung, D.-J. Won, S.-Y. Park, S.-I. Moon, and J.-K. Park, "The DC link energy control method in dynamic voltage restorer system," *Int. J. Elect. Power Energy Syst.*, vol. 25, no. 7, pp. 525–531, Sep. 2003.
- [14] E. C. Aeloíza, P. N. Enjeti, L. A. Morán, O. C. Montero-Hernandez, and S. Kim, "Analysis and design of a new voltage sag compensator for critical loads in electrical power distribution systems," *IEEE Trans. Ind. Appl.*, vol. 39, no. 4, pp. 1143–1150, Jul./Aug. 2003.
- [15] J. W. Liu, S. S. Choi, and S. Chen, "Design of step dynamic voltage regulator for power quality enhancement," *IEEE Trans. Power Del.*, vol. 18, no. 4, pp. 1403–1409, Oct. 2003.
- [16] A. Ghosh, A. K. Jindal, and A. Joshi, "Design of a capacitor supported dynamic voltage restorer for unbalanced and distorted loads," *IEEE Trans. Power Del.*, vol. 19, no. 1, pp. 405–413, Jan. 2004.
- [17] A. Ghosh, "Performance study of two different compensating devices in a custom power park," *Proc. Inst. Elect. Eng.—Gener., Transm. Distrib.*, vol. 152, no. 4, pp. 521–528, Jul. 2005.
- [18] J. G. Nielsen and F. Blaabjerg, "A detailed comparison of system topologies for dynamic voltage restorers," *IEEE Trans. Ind. Appl.*, vol. 41, no. 5, pp. 1272–1280, Sep./Oct. 2005.
- [19] M. R. Banaei, S. H. Hosseini, S. Khanmohamadi, and G. B. Gharehpetian, "Verification of a new energy control strategy for dynamic voltage restorer by simulation," *Simul. Model. Pract. Theory*, vol. 14, no. 2, pp. 112–125, Feb. 2006.
- [20] A. K. Jindal, A. Ghosh, and A. Joshi, "Critical load bus voltage control using DVR under system frequency variation," *Elect. Power Syst. Res.*, vol. 78, no. 2, pp. 255–263, Feb. 2008.
- [21] D. M. Vilathgamuwa, H. M. Wijekoon, and S. S. Choi, "A novel technique to compensate voltage sags in multiline distribution system—The interline dynamic voltage restorer," *IEEE Trans. Ind. Electron.*, vol. 53, no. 5, pp. 1603–1611, Oct. 2006.
- [22] A. Chandra, B. Singh, B. N. Singh, and K. Al-Haddad, "An improved control algorithm of shunt active filter for voltage regulation, harmonic elimination, power-factor correction, and balancing of nonlinear loads," *IEEE Trans. Power Electron.*, vol. 15, no. 3, pp. 495–507, May 2000.
- [23] A. Y. Goharrizi, S. H. Hosseini, M. Sabahi, and G. B. Gharehpetian, "Three-phase HFL-DVR with independently controlled phases," *IEEE Trans. Power Electron.*, vol. 27, no. 4, pp. 1706–1718, Apr. 2012.