Comparison of Power Flow Control in HVDC Link with ANN and PI Controller

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ABSTRACT: This paper describes the simulation of HVDC system which is used to transfer bulk amount of power between two converter stations. The main aim of this paper is to reduce the power loss in the HVDC system. In order to reduce these power losses, we go for power flow control. The power flow control can be done by controlling the rectifier and inverter stations with Artificial Neural Network. The HVDC system simulated in this paper is modelled based on the CIGRE HVDC Benchmark system. An error signal e is derived from the difference between the DC current reference Id_{ref} which is taken from the inverter side and the measured DC current I_d. This error is fed through a ANN controller to produce an alpha-order signal which is used to control the firing pulses to the converter thyristors.

KEYWORDS: HVDC Transmission, Simulation, Artificial Neural Network, Conventional controller.

I.INTRODUCTION

All of the early HVDC schemes were developed using mercury arc valves. The introduction of thyristor valves was demonstrated in 1972 with the first back-to-back asynchronous interconnection at the Eel River between Quebec and New Brunswick. Since then thyristor valve technology has completely replaced mercury arc valve technology. By 2008, a total transmission capacity of 100,000 MW HVDC has been installed in over 100 projects worldwide, more than 25,000 MW HVDC is under construction in 10 projects, and an additional 125,000 MW HVDC transmission capacity has been planned in 50 projects[5]. To account for the rapid growth of DC transmission and its technology it is necessary to include the HVDC transmission into the undergraduate power systems curriculum. Most undergraduate curriculum have only one course on power systems which is typically devoted to AC transmission systems. In accordance with operational requirements, flexibility and investment, HVDC transmission systems can Abe classified into two-terminal and multiterminal HVDC transmission systems. A monopolar link with ground return is usually employed in the HVDC submarine cable scheme, e.g. Konti-Skan, Fenno-Skan, Baltic cable and Kontek HVDC links.Instead of earth or sea return, a monopolar link with metallic return (low insulation) may be used.

An HVDC transmission system consists of three basic parts:
1) A rectifier station to convert AC to DC
2) A DC transmission line and
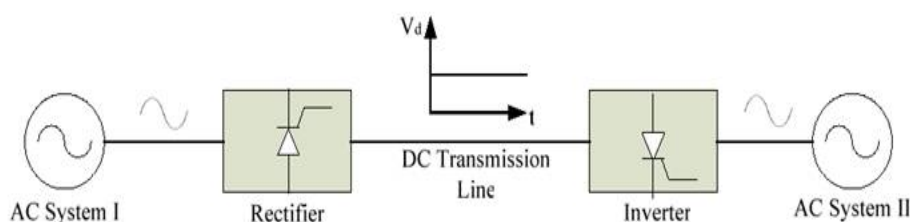3) An inverter station to convert DC back to AC.

Fig1. Schematic diagram of an HVDC transmission system

II. HVDC SYSTEM MODEL

MATLAB/SIMULINK is a high-performance multifunctional software that uses functions for numerical computation system simulation, and application development. Power System Blockset (PSB) is one of its design tools for modelling and simulating electric power systems within the SIMULINK environment. It contains a block library with common components and devices found in electrical power networks that are based on electromagnetic and electromechanical equations. PSB/SIMULINK can be used for modelling and simulation of both power and control systems. PSB solves the system equations through state-variable analysis using either fixed or variable integration time-step. The linear dynamics of the system are expressed through continuous or discrete time-domain state-space equations. It also offers the flexibility of choosing from a variety of integration algorithms.The HVDC system shown in Fig.2. The system is a mono-polar 500-kV, 1000-MW HVDC link with 12-pulse converters on both rectifier and inverter sides, connected to weak ac systems (short circuit ratio of 2.5 at a rated frequency of 50 Hz) that provide a considerable degree of difficulty for dc controls. Damped filters and capacitive reactive compensation are also provided on both sides. The power circuit of the converter consists of the following subcircuits.
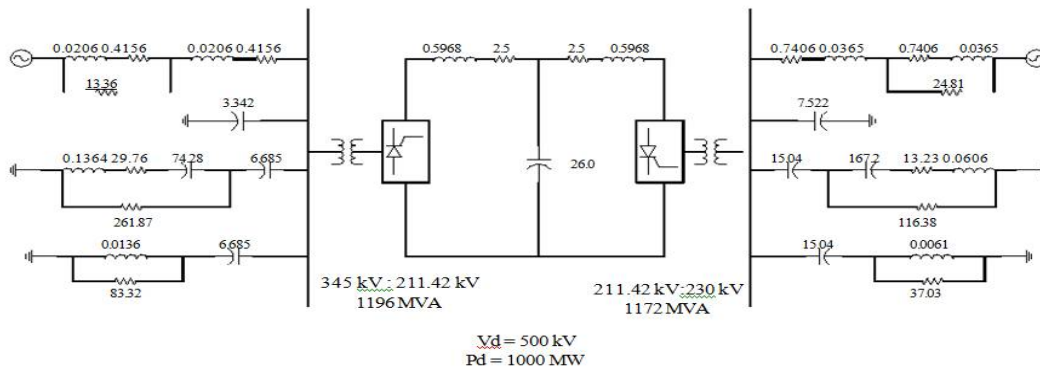

Fig 2. Schematic diagram of an HVDC transmission system

A. AC Side

The ac sides of the HVDC system consist of supply network, filters, and transformers on both sides of the converter. The ac supply network is represented by a Thevenin equivalent voltage source with an equivalent source impedance. AC filters are added to absorb the harmonics generated by the converter as well as to supply reactive power to the converter.

B. DC Side

The dc side of the converter consists of smoothing reactors for both rectifier and the inverter side. The dc transmission line is represented by an equivalent T network, which can be tuned to fundamental frequency to provide a difficult resonant condition for the modelled system.

C. Converter

The converter stations are represented by 12-pulse configuration with two six-pulse valves in series. In the actual converter, each valve is constructed with many thyristors in series. Each valve has a (di/dt) limiting inductor, and each thyristor has parallel RC snubbers.

D. Power Circuit Modeling

The rectifier and the inverter are 12-pulse converters constructed by two universal bridge blocksconnected in series. The converter transformers are modeled by one three-phase two winding transformer with grounded Wye–Wye connection, the other by three-phase two winding transformerwith grounded Wye–Delta connection. The converters are interconnected through a T-network.

1) Universal Bridge Block:

The universal bridge block implements a universal three-phase power converter that consists of six power switches connected as a bridge. The type of power switch and converter configuration can be selected from the dialog box.

Series RC snubber circuits are connected in parallel with each switch device. The vector gating signals are six-pulse trains corresponding to the natural order of commutation. The and measurements are not realized in this model.

2) Three Phase Source:

A three-phase ac voltage source in series with a R-L combination is used to model the source.

3) Converter Transformer Model:

The three-phase two winding transformers models have been used where winding connection and winding parameters can be set through mask parameters. The tap position is at a fixed position determined by a multiplication factor applied on the primary nominal voltage of the converter transformers (1.01 on rectifier side; 0.989 on inverter side). The saturation has been simulated. The saturation characteristic has been specified by a series of current/flux pairs (in p.u.) starting with the pair (0,0).

III. CONTROL VARIABLES FOR CONSTANT POWER FLOW CONTROL

The control model mainly consists of (α/γ) measurements and generation of firing signals for both the rectifier and inverter. The PLO is used to build the firing signals. The output signal of the PLO is a ramp, synchronized to the phase-A commutating.

$$I_d = ((A_r * E_r / T_r) \cos \alpha_r - (A_i * E_i / T_i) \cos \gamma_i) / (R_{cr} + R_d - R_{ci}) \qquad (3.1)$$

$$E_{dr} = (A_r * E_r / T_r) \cos \alpha_r \qquad (3.2)$$

$$E_{di} = (A_i * E_i / T_i) \cos \gamma_i \qquad (3.3)$$

Following are the controllers used in the control schemes:
1. Extinction Angle (γ) Controller
2. dc Current Controller;
3. Voltage Dependent Current Limiter (VDCOL).

1) Rectifier Control:

The rectifier control system uses Constant Current Control (CCC) technique. The reference for current limit is obtained from the inverter side. This is done to ensure the protection of the converter in situations when inverter side does not have sufficient dc voltage support (due to a fault) or does not have sufficient load requirement (load rejection). The reference current used in rectifier control depends on the dc voltage available at the inverter side. Dc current on the rectifier side is measured using proper transducers and passed through necessary filters before they are compared to produce the error signal. The error signal is then passed through a PI controller, which produces the necessary firing angle order. The firing circuit uses this information to generate the equidistant pulses for the valves using the technique described earlier.

2) Inverter Control:

The Extinction Angle Control or γcontrol and current control have been implemented on the inverter side. The CCC with Voltage Dependent Current Order Limiter (VDCOL) have been used here through PI controllers. The reference limit for the current control is obtained through a comparison of the external reference (selected by the operator or load requirement) and VDCOL (implemented through lookup table) output. The measured current is then subtracted from the reference limit to produce an error signal that is sent to the PI controller to produce the required angle order. The γcontrol uses another PI controller to produce gamma angle order for the inverter. The two angle orders are compared, and the minimum of the two is used to calculate the firing instant.

IV. ARTIFICIAL NEURAL NETWORKS

Numerous advances have been made in developing intelligent systems, some inspired by biological neural networks]. Researchers from many scientific disciplines are designing artificial neural networks to solve a variety of problems in pattern recognition, prediction, optimization, associative memory, and control. Conventional approaches have been proposed for solving these problems. Although successful applications can be found in certain well-constrained

environments, none is flexible enough to perform well outside its domain. ANNs provide exciting alternatives, and many applications could benefit from using them. This article is for those readers with little or no knowledge of ANNs to help them understand the other articles in this issue of Computer.

V. RESULT AND DISCUSSION

In the fig 3, it shows thesimulation model of the HVDC System which consists of AC Systems, DC link, Converter Stations and control blocks for both rectifier and inverter stations
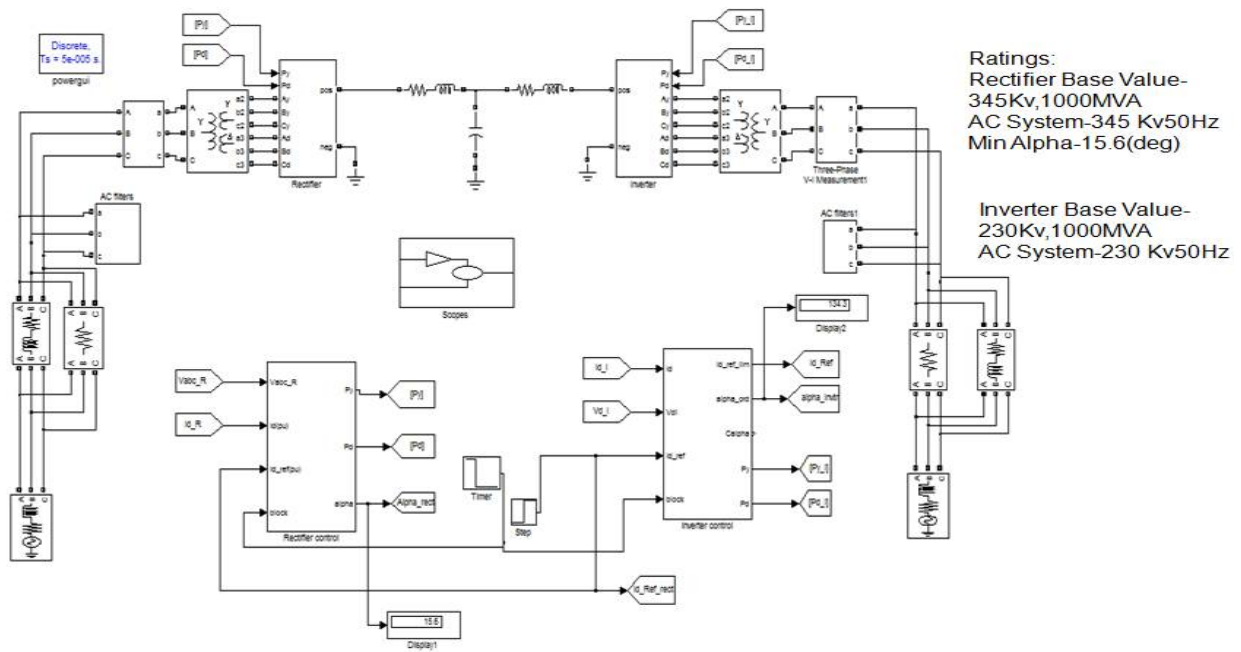
Fig 3. Simulink model of HVDC System

In the fig 4, it shows thegraph of three phase voltage and currents of Rectifier AC side are sinusoidal which are represented in per units (p.u).
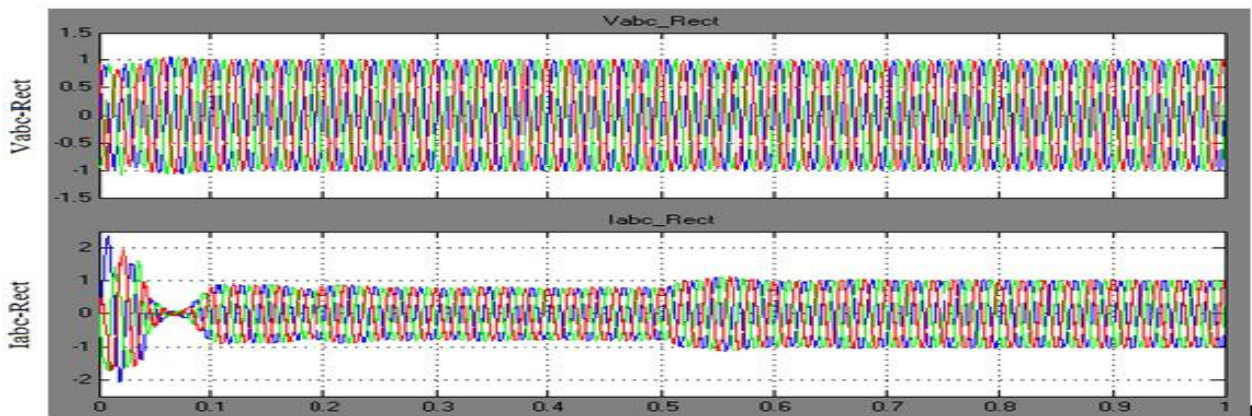
Fig 4. Rectifier side AC Voltage ad AC Current

In the fig 5, it shows thegraph ofthree phase voltage and currents of Inverter AC side are sinusoidal which are represented in per units (p.u).
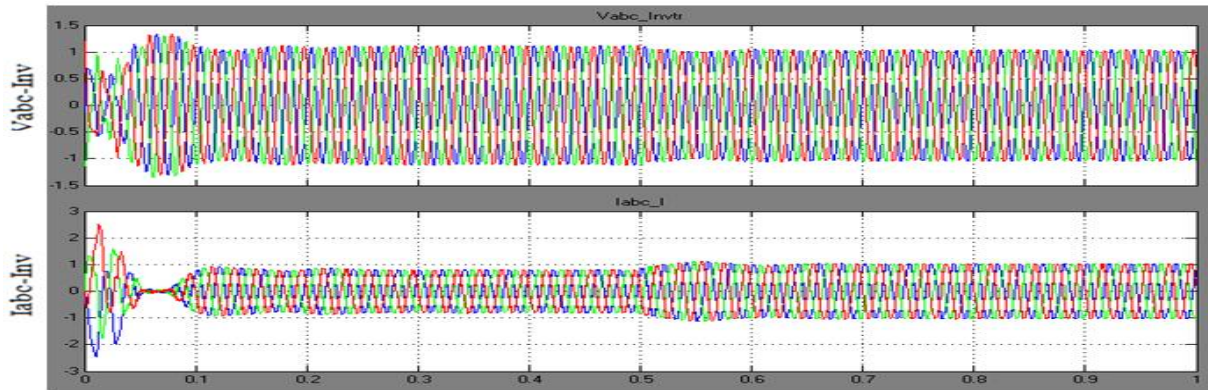

Fig 5. Inverter side AC Voltage ad AC Current

In the fig 6, it shows thegraph of I_{d_R} and I_{d_Ref} (Rectifier side DC) are compared to produce an error signal which gives the firing angle order (α=15.5 deg).
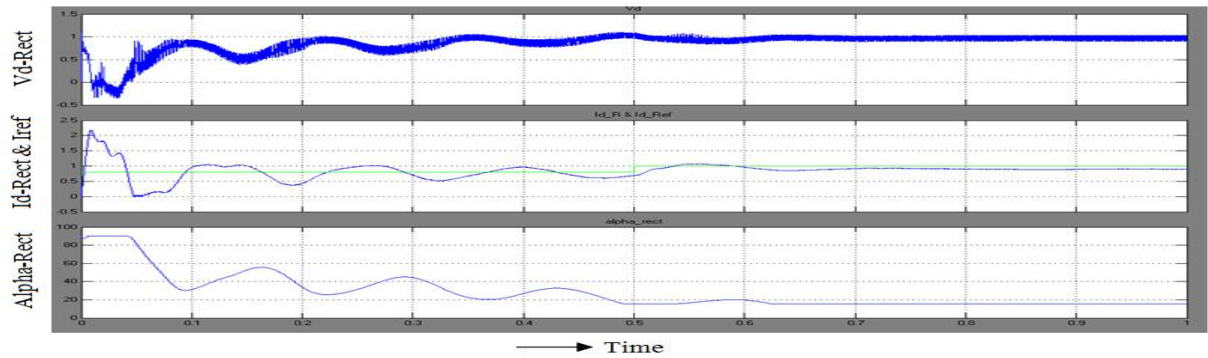

Fig 6. Rectifier side DC Voltage, DC Current and firing angle order with PI

In the fig 7, it shows thegraph of I_{d_I} and I_{d_Ref}(Inverter side DC) are compared to produce an error signal which gives the firing angle order(α_{inv}=134 deg).
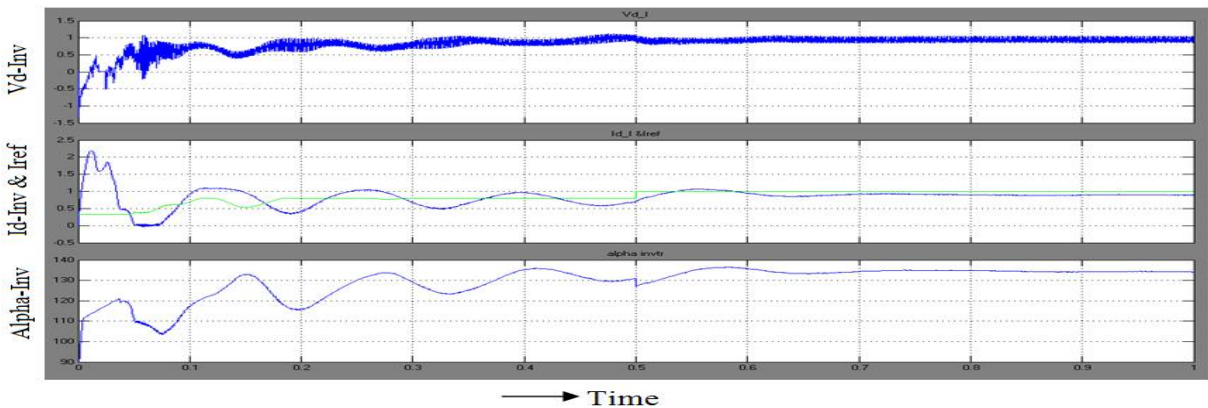

Fig7. Inverter side DC Voltage, DC Current and firing angle order with PI
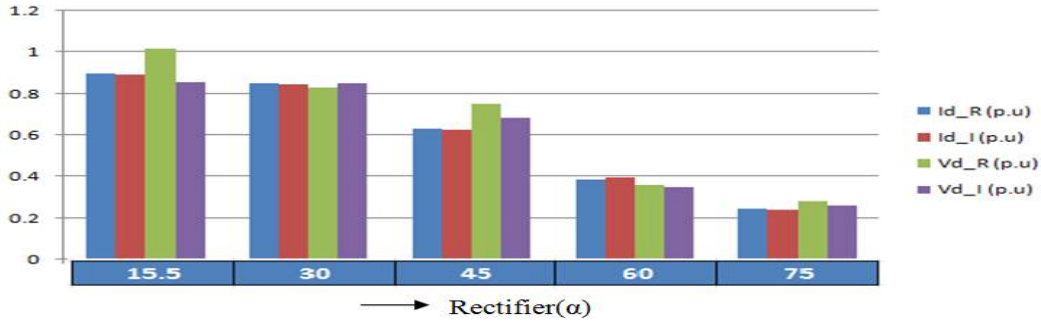
Fig 8. Effect due to change in Rectifier firing angle (chart representation) using PI

From the above chart, as the rectifier firing angle α increases, the DC currents and DC voltages of both rectifier and inverter are decreases.

In the fig 9, it shows thegraph of I_{d_R} and I_{d_Ref} (Rectifier side DC) are compared to produce an error signal which gives the firing angle order (α=15.5 deg).
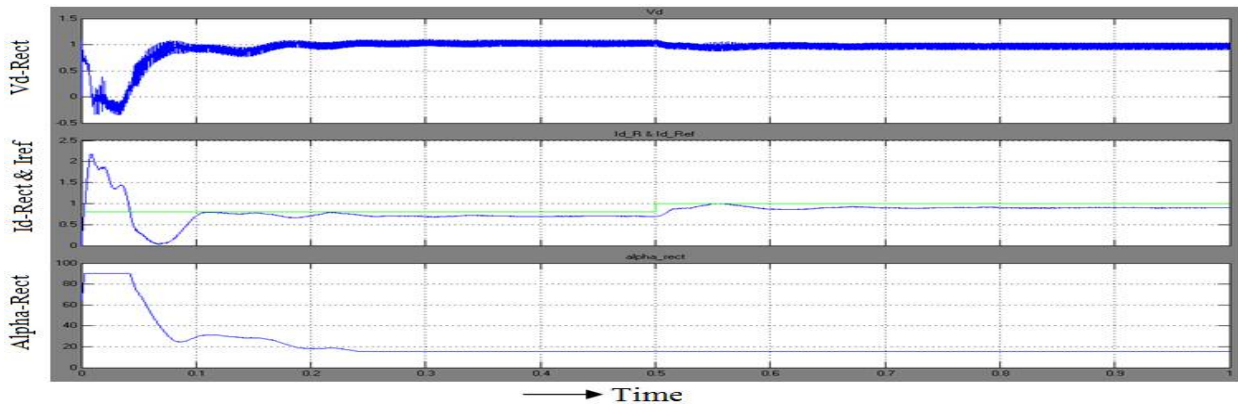

Fig 9. Rectifier side DC Voltage, DC Current and firing angle order with Artificial Neural Network

In the fig 10, it shows thegraph of I_{d_I} and I_{d_Ref}(Inverter side DC) are compared to produce an error signal which gives the firing angle order($α_{inv}$=142deg).
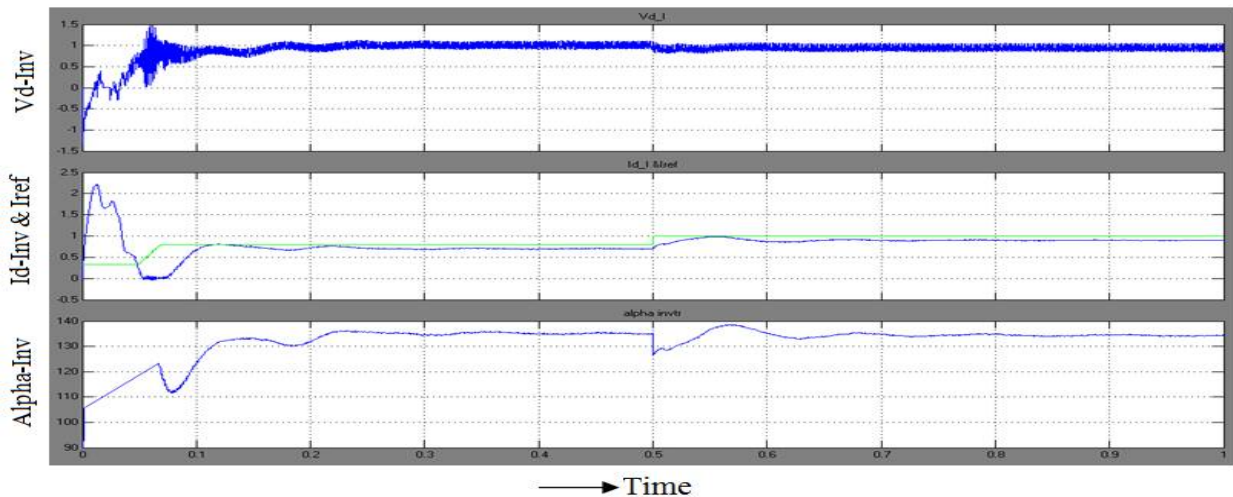

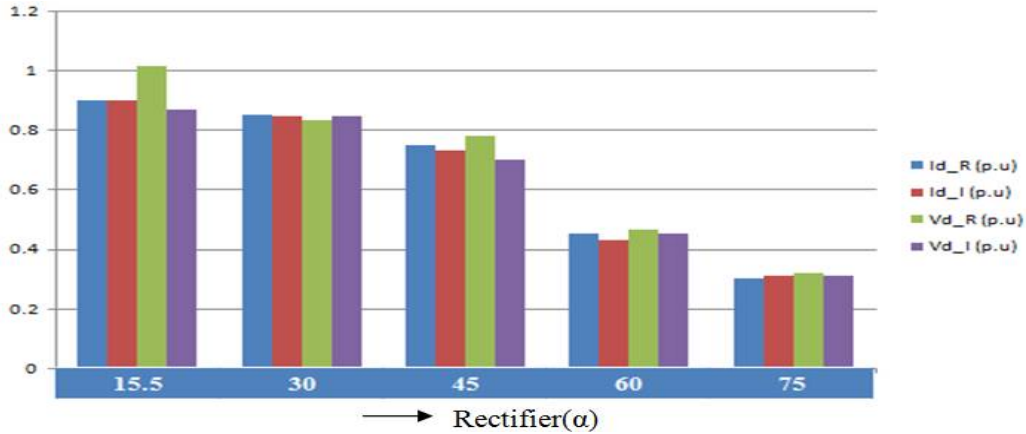Fig 10.Inverter side DC Voltage, DC Currentand firing angleorder with Artificial Neural Network

Fig 11. Effect due to change in Rectifier firing angle (chart representation) using ANN

From the above chart using Artificial NeuralNetworks as the rectifier firing angle α increases, the DC currents and DC voltages of both rectifier and inverter are decreases.

TABLE 1. Comparison between PI and Artificial Neural Network for different Firing Angles

Rectifier α (deg)	Inverter α (deg)		Id_R (p.u)		Id_I (p.u)		Vd_R (p.u)		Vd_I (p.u)	
	PI	ANN	PI	ANN	PI	ANN	PI	ANN	PI	ANN
15.5	134.3	142	0.8954	0.9030	0.8990	0.9024	1.0160	1.0190	0.8587	0.8690
30	128.6	130	0.8496	0.8520	0.8440	0.8480	0.8300	0.8350	0.8400	0.8470
45	119.4	120.5	0.6294	0.7530	0.6261	0.7340	0.7490	0.7840	0.6825	0.7020
60	109.9	112	0.3848	0.4520	0.3989	0.4320	0.3580	0.4680	0.3500	0.4520
75	98.62	101	0.2469	0.3010	0.2394	0.3120	0.2800	0.3210	0.2600	0.3120

From the above table, at $\alpha = 30^0$ the DC currents and voltages of both rectifier and inverter with Artificial Neural Network shows better values when compared with PI controller.

Similarly from the above tablethe DC currents and voltages of both rectifier and inverter with Artificial Neural Network shows better values when compared with PI controller for different firing angles.

VI. CONCLUSION

In this paper, a HVDC system is designed to control the power flow between two converter stations with conventional controller and Artificial Neural Networks. For rectifier side current control is used and for inverter side both current and extinction angle control is implemented. In order to transfer maximum power in the DC link, we have to maintain minimum alpha. The error signal is passed through a PI and Artificial Neural Networks controller, which produces the necessary firing angle order.The firing circuit uses this information to generate the equidistant pulses for the valves in the converter station. Here Artificial Neural Networks is designed for both rectifier and inverter control and compared its performance with conventional controller.The simulation results show that the HVDC with Neural network based controller have great advantage of flexibility when compared with PI controller.

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