



# **High DC Voltage Gain with Input Power Factor Correction**

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**ABSTRACT:** In this paper a new integrated ac/dc converter is presented. A high voltage gain converter with power factor correction integrates the operation of active power factor correction in the input side and high voltage gain dc-dc converter. The input side have the boost power factor correction and high dc voltage is obtained at the output. The converter operates as a single stage converter that performs PFC/AC-DC conversion and DC-DC conversion. DC-DC converter is a part of plug in AC-DC converter. The paper explains the operation of the converter in details.

**KEYWORDS:** AC/DC conversion, Active power factor correction, High voltage gain converter, Diode rectifier.

## **I. INTRODUCTION**

AC/DC converters are having many important applications in many offline power supplies. Traditionally, AC/DC converters consists of a diode bridge rectifier, followed by a bulky capacitor and a high frequency DC/DC converter. This kind of converter inevitably introduces a highly distorted input current, resulting in a large amount of harmonics and low power factor. AC/DC are widely used in different types of electronic devices such as battery charger, electric vehicles, and in portable devices like cellular phones, laptops, computers etc. The electrical supply industry has placed requirements on the power factor of electrical equipment for many years. Historically, these requirements were developed around powered equipment consisting of resistive and reactive (inductive or capacitive) loads, which will present varying phase angles between the sinusoidal voltage applied to the load and the current flowing in it. With a purely resistive load the current and voltage are in phase so the real power consumed is just the product of voltage and current. However, with reactive elements there will be a phase shift between the current and voltage. For a linear load, Power factor is defined as follows: Power Factor (PF) = Real Power / (RMS Voltage x RMS Current).

Power factor is described as leading for capacitive loads (i.e., current builds up faster than voltage) and lagging for inductive loads (i.e., current builds up slower than voltage). Financial penalty charges will often be imposed on loads that violate requirements. A high voltage AC-DC converter with power factor correction integrates the operation of the boost power factor correction and high voltage DC-DC converter. The input side have the active power factor correction and high DC voltage is obtained at the output. The converter operates as a single stage converter that performs PFC/AC-DC conversion and DC-DC conversion. The DC/DC converter is part of a plug-in AC/DC converter. AC power is rectified by four diodes from a bipolar wave shape to a unipolar shape. Higher power AC-DC converters are required to have some sort of power factor correction (PFC) capability. PFC methods can generally be divided into the following three categories: passive PFC converters, two stage converter and single. Passive PFC converters use passive elements such as inductors and capacitors to filter low frequency input current harmonics and make the input current more sinusoidal. Two-stage converters consist of an AC-DC boost pre-regulator converter that shapes the input current and an isolated dc-dc full-bridge that converts the pre-regulator output into the required dc voltage. Single-stage converters can perform PFC/ac-dc conversion and dc-dc conversion with just a single full bridge converter. The paper introduces the converter that explains its basic operating principles and its modes of operation, discusses its features and its design. The feasibility of the new converter is confirmed with experimental results obtained from a prototype converter.

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## II. LITERATURE SURVEY

In the literature, many techniques about power factor correction have been mentioned.

Ma et al. [4] proposes a converter for the adapter application, which is composed of a flyback converter, a feedback winding and a front-end input current shaper. Through the feedback winding, a direct energy transfer path is configured to improve the conversion efficiency.

Wang et al. [5] explain a dc power supply system to give high power factor and low current distortion on the rectifier side and provide stable dc voltage on the isolated dc/dc converter side.

Zhao et al. [1] proposes a converter that integrate a boost-derived input current shaper with a forward dc/dc converter in one single stage. The bulk capacitor voltage feedback with a coupled winding structure is used to reduce both the voltage and current stresses.

Agamy et al. [6] explain about current-fed converters with a boost inductor connected to the input of the full-bridge circuit. Although they can achieve a near-unity input power factor, they lack an energy-storage capacitor across the primary-side dc bus, which can result in the appearance of high voltage overshoots and ringing across the dc bus. It also causes the output voltage to have a large low-frequency 120-Hz ripple that limits their applications.

In this paper we take motivation from ref. [2, 3 and 7] and which mention about the single stage converters that can perform PFC/ac-dc conversion and dc-dc conversion with just a single fullbridge converter. There have been numerous publications about single-stage PFC (SSPFC) converters particularly for low-power ac-dc flyback and forward converters

## III. CONVERTER TOPOLOGY

The converter, which is shown in fig 1, integrates an AC/DC boost PFC converter into a high voltage DC-DC converter. The AC-DC boost section consists of an input diode bridge, boost inductor  $L_{in}$ , boost diode  $D_{x1}$  and switch  $S_4$ , which is shared by the multilevel DC-DC section. When  $S_4$  is off, it means that no more energy can be captured by the boost inductor. In this case diode  $D_{x2}$  prevents input current from flowing to the mid-point of capacitors  $C_1$  and  $C_2$  and diode  $D_{x1}$  conducts and helps to transfer the energy stored in the boost inductor  $L_{in}$  to the dc bus capacitor. Diode  $D_{x3}$  bypasses  $D_{x2}$  and makes a path for circulating current. Equivalent circuit diagrams that show the converter's modes of operation are shown with the diode rectifier bridge output replaced by a rectified sinusoidal source.

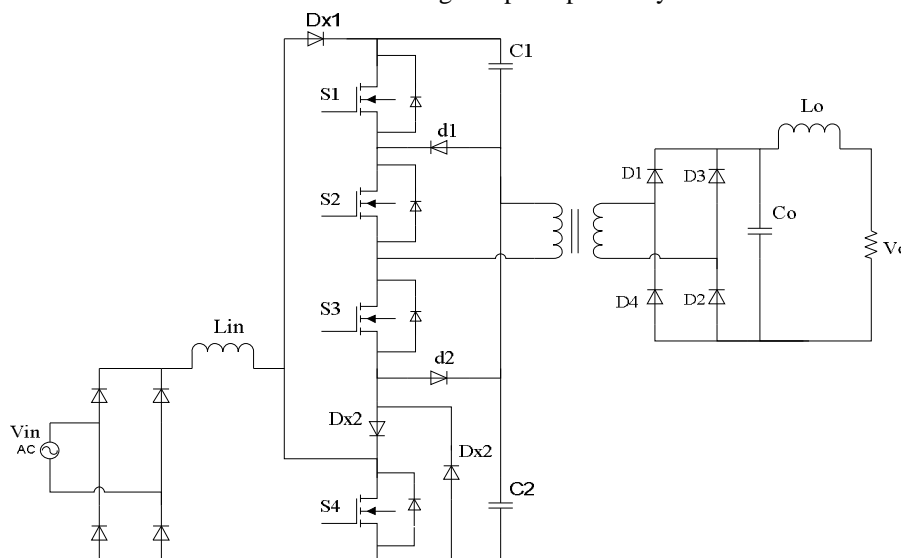


Fig.1: High Voltage AC-DC Converter with Power Factor Correction

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## IV.MODES OF OPERATION

**Mode 1 ( $t_0 < t < t_1$ ):** During this mode, switches S1 and S2 are on and energy from DC bus capacitor C1 is transferred to the output load. Positive voltage is impressed across  $L_o$  and the current through it rises.

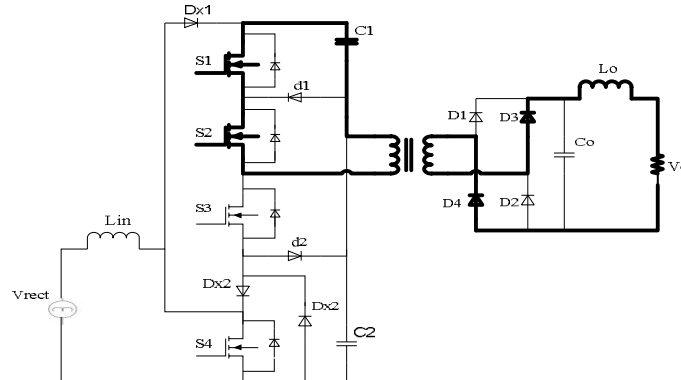


Fig.2. Active components in mode 1

**Mode 2 ( $t_1 < t < t_2$ ):** In this mode, S1 and S2 remain on and S4 turns on. The diode bridge output voltage  $V_{rect}$  is impressed across input inductor  $L_{in}$  so that the current flowing through this inductor rises. The energy from dc bus capacitor C1 is transferred to the output load.

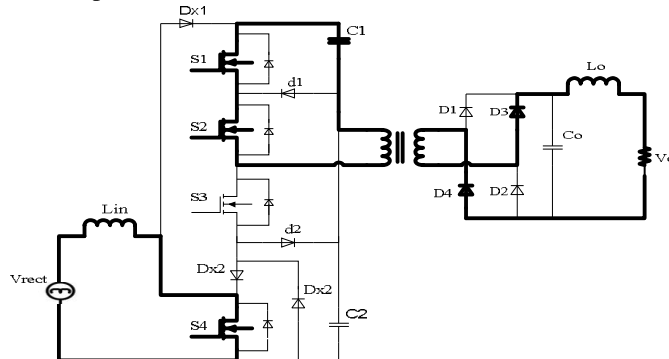


Fig.3. Active components in mode 2

**Mode 3 ( $t_2 < t < t_3$ ):** In this mode, S2 remain on and S4 turns on. At the same time, the diode bridge output voltage  $V_{rect}$  is impressed across input inductor  $L_{in}$  so that the current flowing through this inductor rises.  $V_{rect}$  is impressed across input inductor  $L_{in}$  so that the current flowing through this inductor rises.

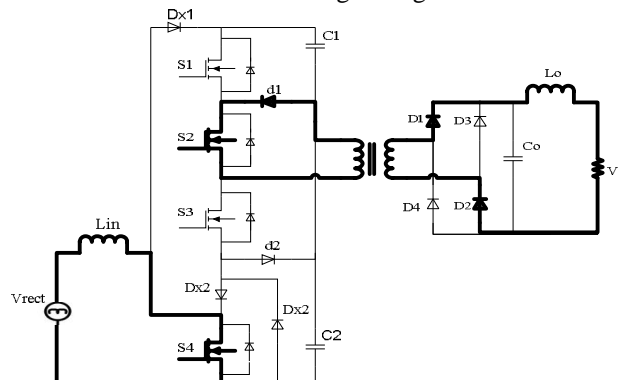


Fig.4. Active components in mode 3

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**Mode 4 ( $t_3 < t < t_4$ ):** In this mode, S1 and S2 are OFF and S4 is ON. The current in the primary of the transformer charges capacitor C2 through the body diode of S3 and Dx3.

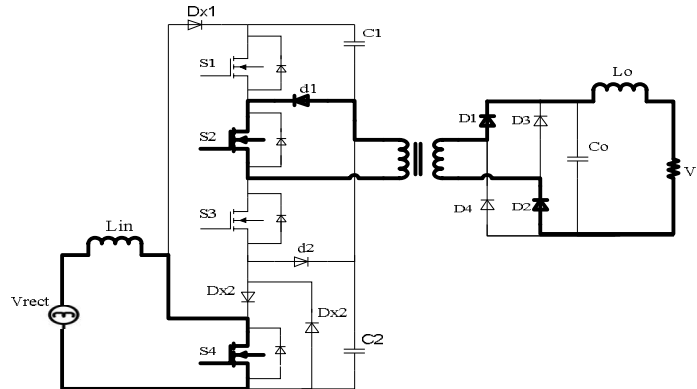


Fig.5. Active components in mode 4

**Mode 5 ( $t_4 < t < t_5$ ):** In this mode, S3 and S4 are ON. Energy flows from capacitor C2 flows into the load while the current flowing through input inductor  $L_{in}$  continues to rise.

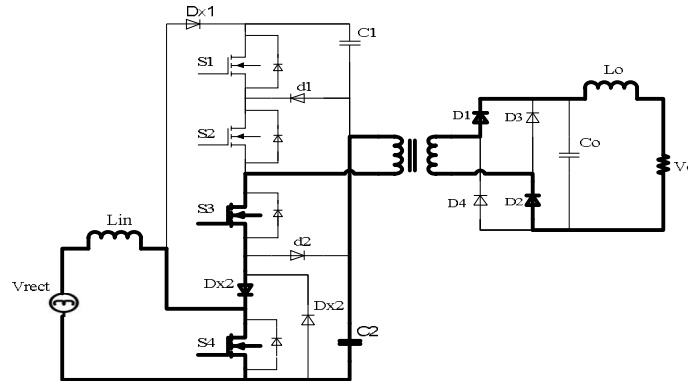


Fig.6. Active components in mode 7

**Mode 6 ( $t_5 < t < t_6$ ):** In this mode, S4 turns off. The current in input inductor flows through the diode Dx1 to charge the capacitors C1 and C2. The current in the transformer primary flows through the S3 and D2. This mode ends when the inductor current reaches zero. Also during this mode, the load inductor current freewheels in the secondary of the transformer.

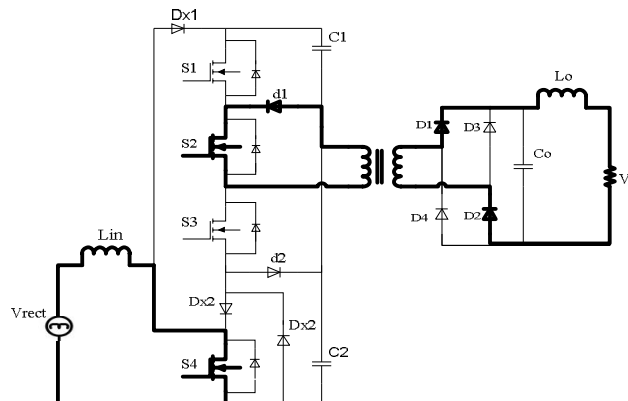


Fig.7. Active components in mode 6

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**Mode 7 ( $t_6 < t < t_7$ ):** In this mode, the load inductor current freewheels in the secondary of the transformer. This mode ends when the switches S3 turns off.

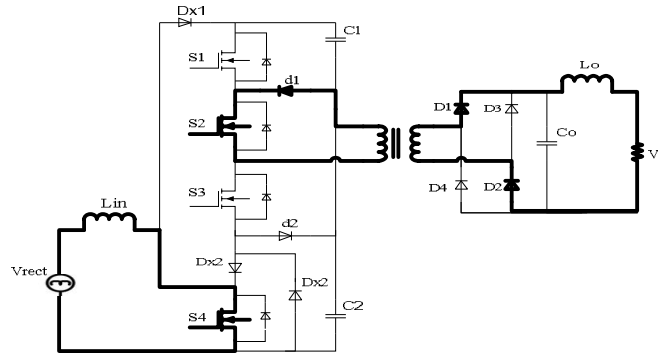


Fig.8. Active components in mode 7

**Mode 8 ( $t_7 < t < t_8$ ):** In this mode, S3 is OFF and the current in the primary of the transformer charges capacitor C1 through the body diodes of S1 and S2. Finally, converter re-enters Mode 1.

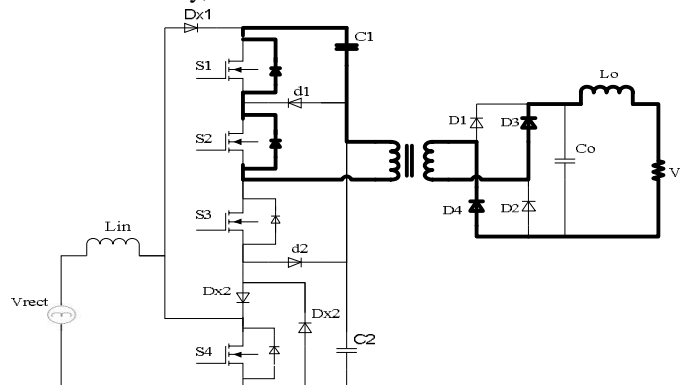


Fig.9. Active components in mode 8

## V.SIMULATION RESULTS

There are number of simulation software available and here the simulink part of the MATLAB is employed. Simulation of the AC/DC converter topology is performed using matlab. All input dc sources are equal. MATLAB (R2013a) is used for simulation part of the work.

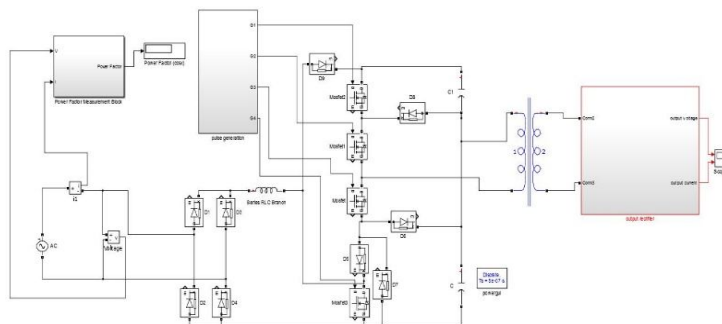


Fig.10. Simulation Diagram of Converter

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A prototype of the proposed converter was simulated through MATLAB. The converter was tested with  $V_{in} = 190V$ ,  $V_o = 285V$  is shown in fig 10, further design parameters are given in Table I. Input inductor is the boost inductor which is take 4mH inductance and c1 and c2 are the dc bus capacitors.

Table I. Design Specifications

	PARAMETER	VALUE
<b>Lin</b>	<b>Input inductor</b>	<b>4mH</b>
<b>C1,C2</b>	<b>Capacitor</b>	<b>2200<math>\mu</math>F</b>
<b>Lo</b>	<b>Output inductor</b>	<b>10<math>\mu</math>H</b>
<b>Co</b>	<b>Output capacitor</b>	<b>820<math>\mu</math>F</b>

The main advantage of this convertor is the power factor correction at the input side of the converter. In this section the input voltage and input current is measured and given to the power factor measurement block. Input voltage and current is converted to its rms value to find the power factor.

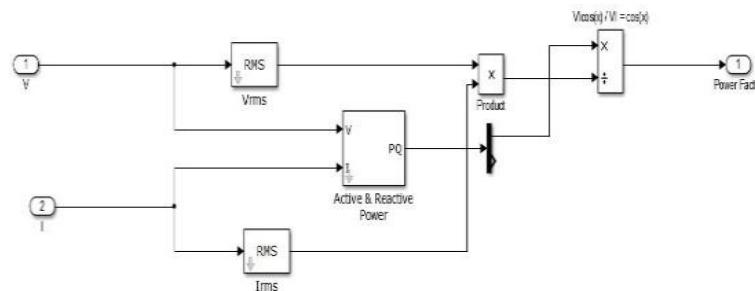
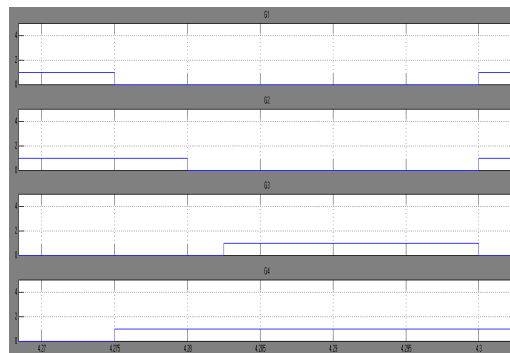


Fig.11. Power Factor Measurement

Typical converter waveforms are shown in fig 12, The gating signals for S2 and S3 are generated in such a way that both switches are each on for half a switching cycle, but are never on at the same time.



Time in (seconds)

Fig.13. Generated pulses

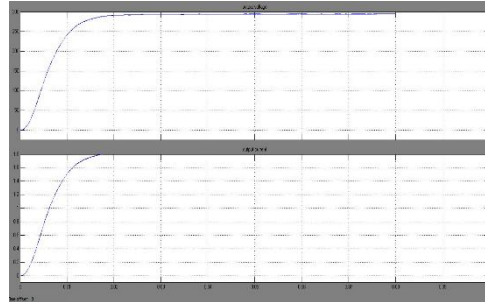


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The output voltage and current obtained is shown in fig 14, 285V dc is obtained at the output of the converter.



Time in (seconds)

Fig.13. Output waveform

## VI.CONCLUSION

A new single-stage ac-dc converter is presented in the paper. The converter performs input PFC and high dc output voltage. The active power factor correction method is used for the input side and obtain a power factor of 0.97. The outstanding feature of this converter is that it combines the performance of two-stage converters with the reduction of cost of single-stage converters. The paper explains its basic operating principles and modes of operation. Experimental results that confirm the feasibility of the converter are also presented in the paper.

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